

Model Name: GA-H110M-D3H

SHEET

TITLE

01	COVER SHEET
02	BOM & PCB MODIFY HISTORY
03	BLOCK DIAGRAM
04	CPU_LGA1150-A
05	CPU_LGA1150-B_DDR4
06	CPU_LGA1150-C
07	CPU_LGA1150-D
08	DDR4 CHANNEL A 1,2 (REV0.6)
09	DDR4 CHANNEL B 1,2
10	PCH_RGB,CLK_BUFFER (REV0.7)
11	PCH_DMI,USB,PCIE
12	PCH_MISC
13	PCH_SATA,PCIE
14	PCH_PWR,GND
15	DUAL BIOS (REV0.1)
16	ITE 8628 LPC IO (REV1.09)
17	HMW
18	FAN CTRL--SIO (REV0.73)
19	PCI EXPRESS X16 SLOT
20	PCI EXPRESS X4 SLOT
21	ISL95858 PWM-IRON (REV0.15)
22	ISL95858 MOS_VCORE-IRON
23	ISL95858 MOS_VCCGT-IRON
24	VCCSA_VCCIO_VCCPLL (REV0.4)(REV0.22)
25	RT8237_DDR-BEAD (REV0.4)
26	RT8068A_VPP-IRON
27	RT8237_PCH-BEAD-IRON
28	DISCRETE POWER (REV0.51)

Rev 1.0

SHEET

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30	KB_MS_USB (REV0.73)
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32	RTD2168 - DP to VGA - Conn
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34	HDMI Conn (REV0.73)
35	R_USB30 (REV0.73)
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37	REALTEK RTL8111G (REV1.06)
38	USB30_LAN CONNECTOR-8111G
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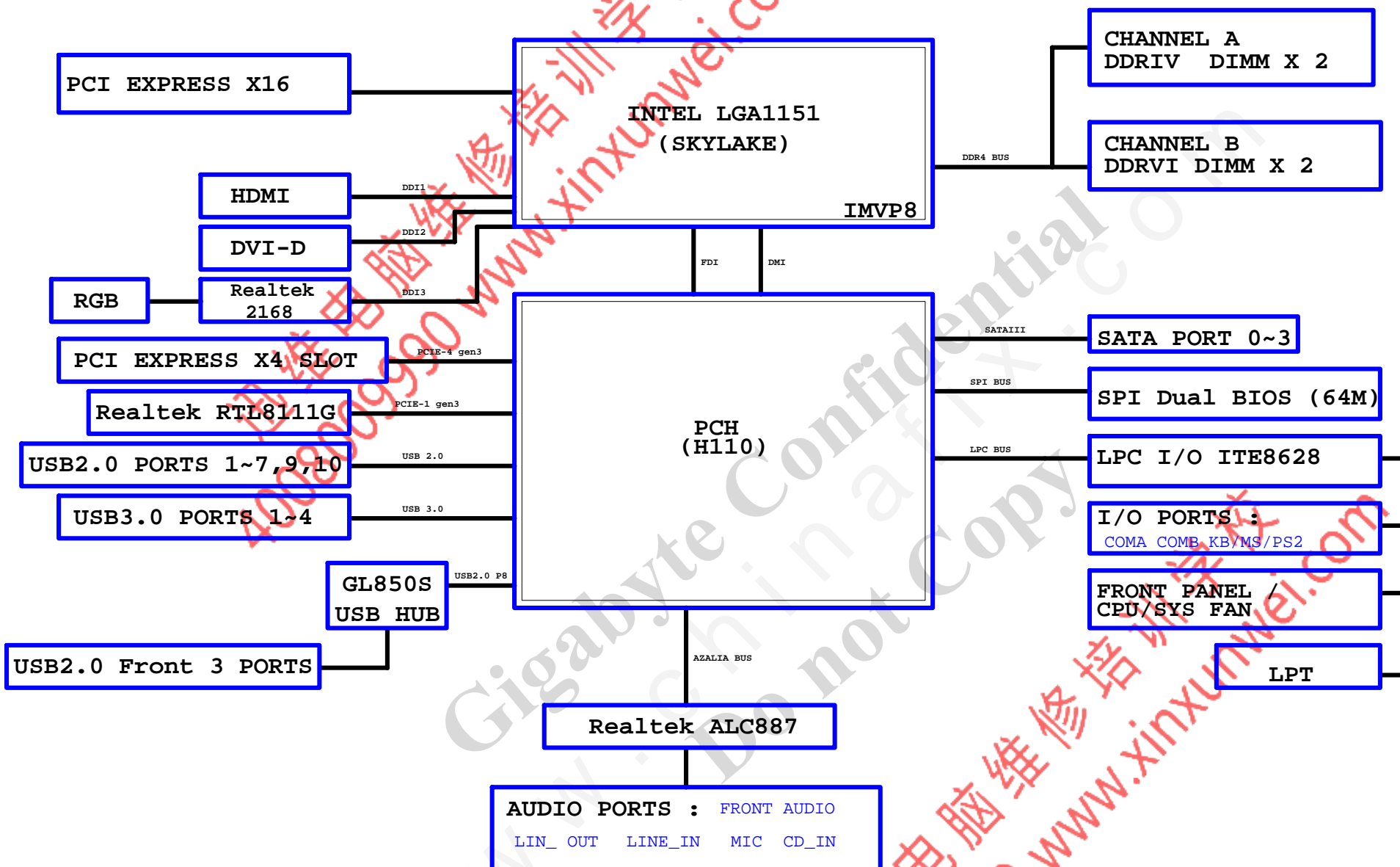
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Size	Document Number	GA-H110M-D3H	Rev 1.0
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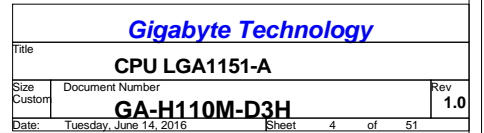
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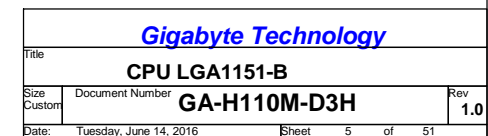
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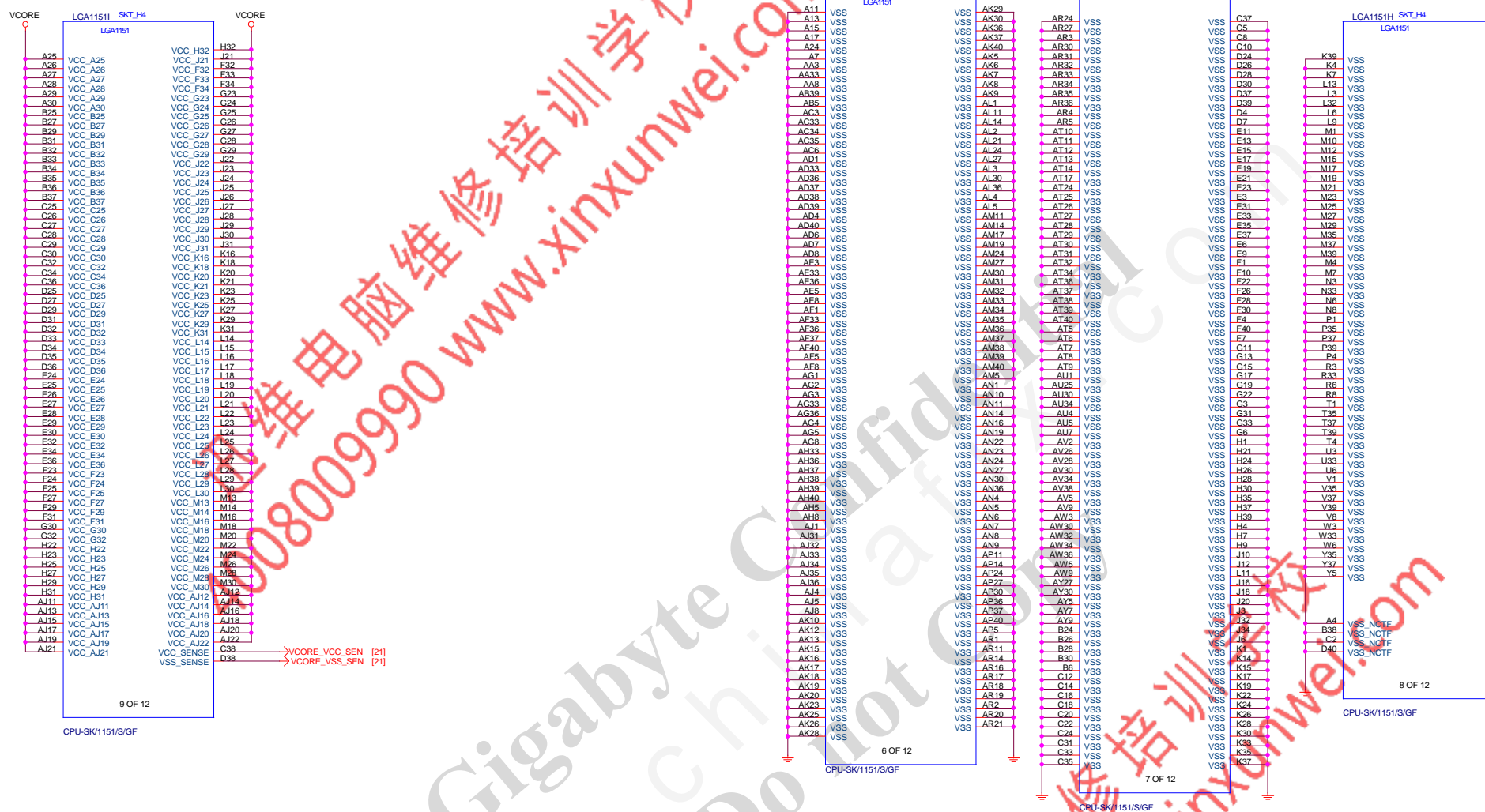
BLOCK DIAGRAM

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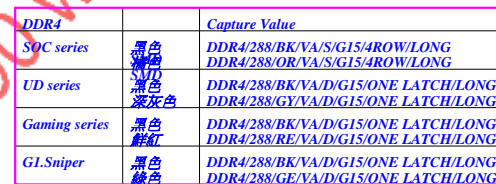






* 刪 Vcore 電容

* 改DDR4 SHARE net



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* 改DDR4 SHARE net

- [5] MDIO_03 ← MDIO_03
- [5] MAA0P_10 ← MAA0P_10
- [5] M_DQSB0_7 ← M_DQSB0_7
- [5] M_DQSB0_7 ← M_DQSB0_7
- [5] M_DQSB0_7 ← M_DQSB0_7
- [5] M_DQSB0_7 ← M_DQSB0_7

VPP_25V

12V3_NC

MAC15 0.1uA/XTX16V/K VREF_DQB

SBAB1

M_DQSB0

MAA0P_10

MAA0P_10

MAA0P_10

MAA0P_10

CHANNEL B0
SA2:0=010

灰色

* 改DDR4 SHARE net

- [5] MDIO_03 ← MDIO_03
- [5] MAA0P_10 ← MAA0P_10
- [5] M_DQSB0_7 ← M_DQSB0_7
- [5] M_DQSB0_7 ← M_DQSB0_7
- [5] M_DQSB0_7 ← M_DQSB0_7
- [5] M_DQSB0_7 ← M_DQSB0_7

VPP_25V

12V3_NC

MAC15 0.1uA/XTX16V/K VREF_DQB

SBAB1

M_DQSB0

MAA0P_10

MAA0P_10

MAA0P_10

MAA0P_10

CHANNEL B1
SA2:0=011

黑色

* 改DDR4 SHARE net

- [5] MDIO_03 ← MDIO_03
- [5] MAA0P_10 ← MAA0P_10
- [5] M_DQSB0_7 ← M_DQSB0_7
- [5] M_DQSB0_7 ← M_DQSB0_7
- [5] M_DQSB0_7 ← M_DQSB0_7
- [5] M_DQSB0_7 ← M_DQSB0_7

VPP_25V

12V3_NC

MAC15 0.1uA/XTX16V/K VREF_DQB

SBAB1

M_DQSB0

MAA0P_10

MAA0P_10

MAA0P_10

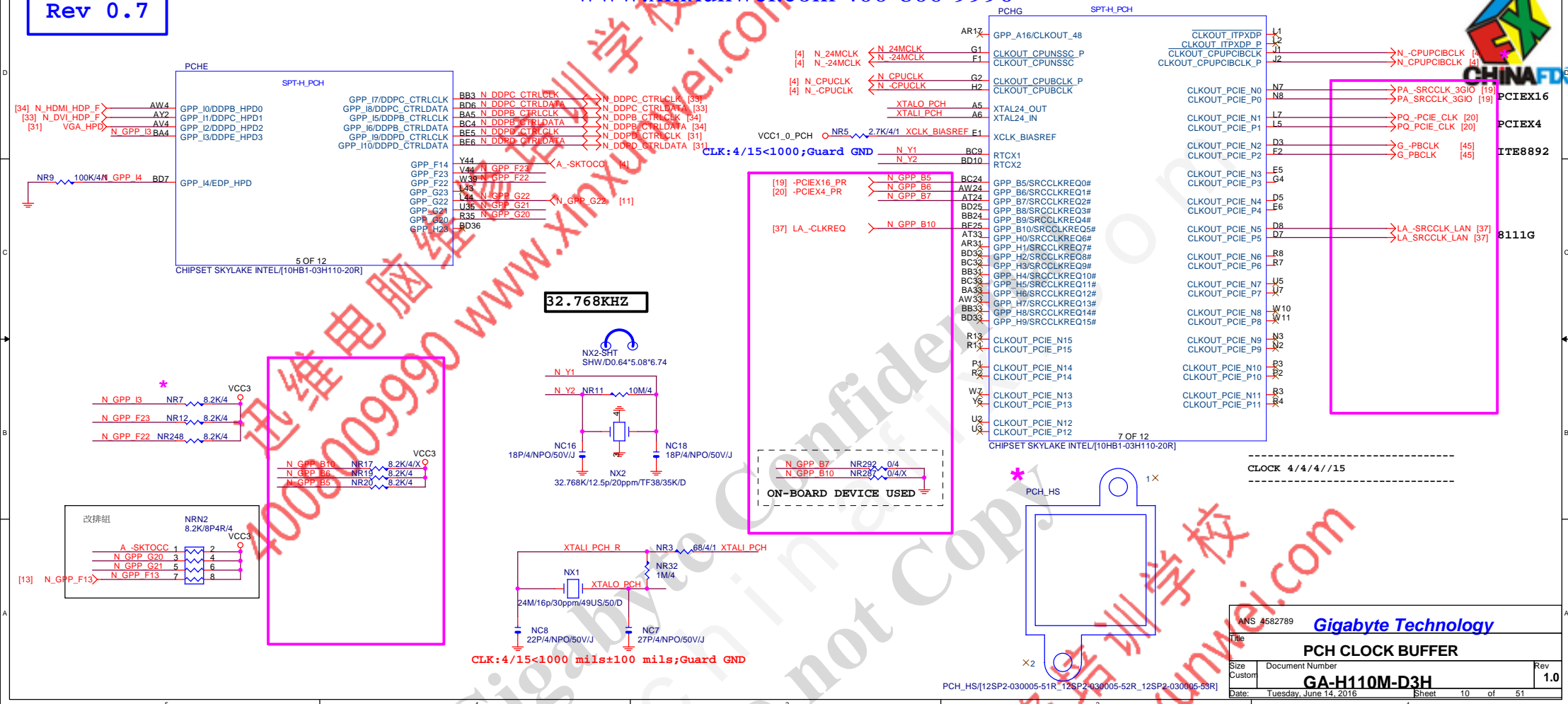
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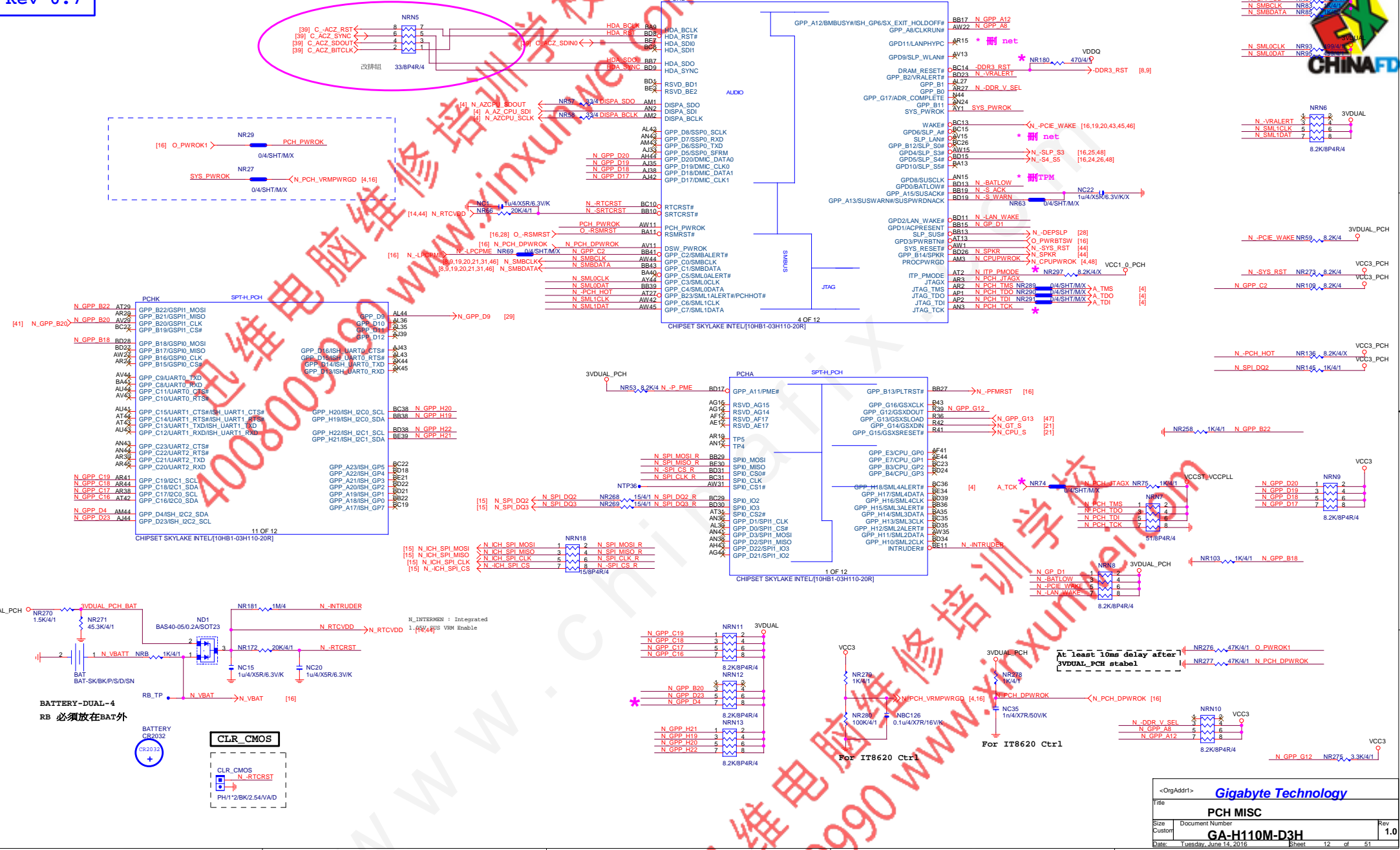
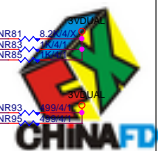
The same to Z170

CPU

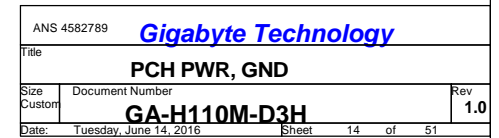
- DTMM4
 - DTMM2
 - DTMM3
 - DTMM1
- CHA
- CHB

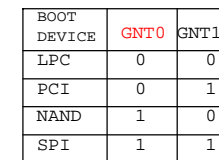
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File	DDR4 CHANNEL B	
Size	Document Number	Rev
GA-H110M-D3H		1.0
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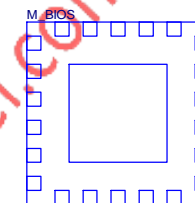








1 means floating
0 means PD 1K

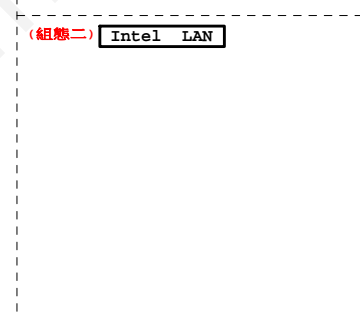
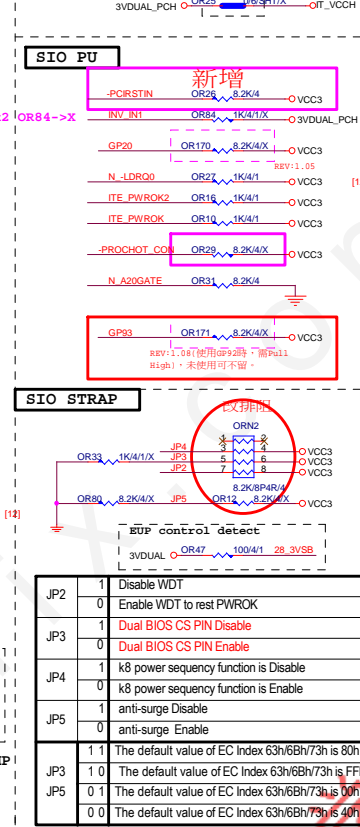


LCP/G-FL/1.27mm/200MIL/WHITE[10SL2-000008-31R]/X

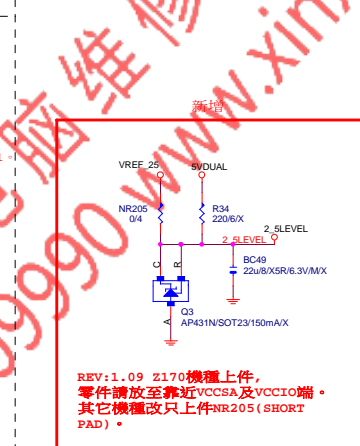
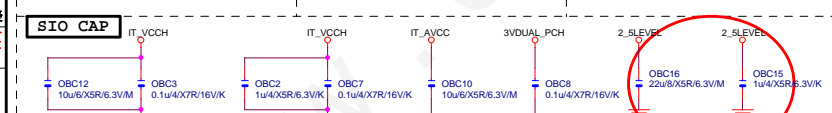
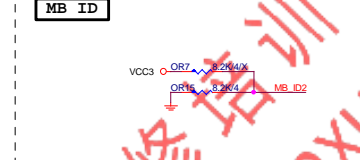
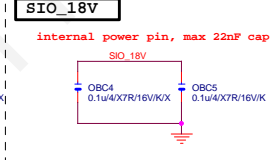
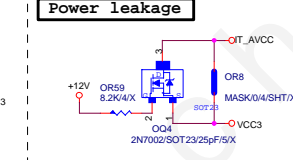
* 試産先上 , PVT 移除

BIOS PH

* Del BIOS PH

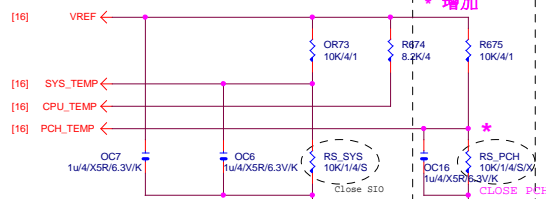


IT8620U GPIO問題演義	
PIN 50	GP26---第一次接上POWER時 會拉 Lo
PIN 90/91	DEFAULT為HDLDED FUNCTION GP3 BYPASS TO GP92 高溫時 GP2 會被拉 Lo(ITE BUG)
PIN 108	GP40--- POWER ON 時會拉 Lo
111/112	MOUSE 跟FANG FUNCTION 擇一使用，不然會互相干擾
PIN 22	FIN22，需高於3V，若低於 該部分CMOS PORT及LPT 端機器會異常動作。

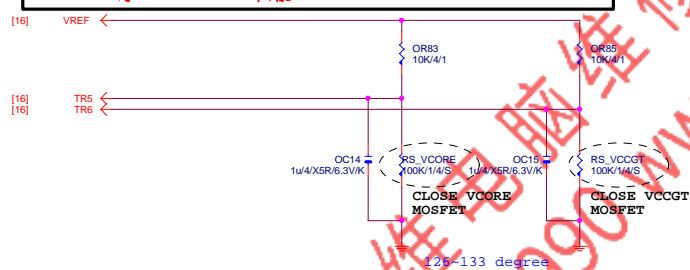


ERP Wake on LAN		
Single LAN	Realtek	組態一
	Atheros	
	Intel 219	組態二
Dual LAN	Atheros+Atheros	組態一
	Intel 219+Atheros	組態三
	Intel 219+Intel 210	
No Support ERP	BOM不上	N/A

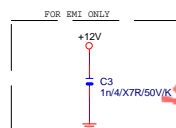
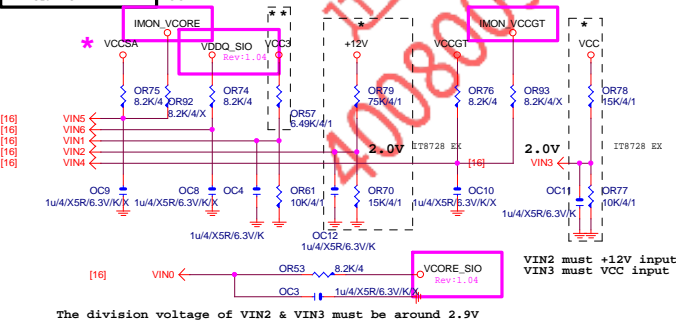
TEMP H/W MONITOR



RS_VCORE, RS_VCCGT, CLOSE CPU_VCORE & VCCGT MOSFET

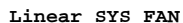


VOLTAGE-- H/W MONITOR



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Title			HWM,KB/MS, FAN CTRL
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Enable Function (NCT3941S)
Full Turn On Function (NCT3941S-A)



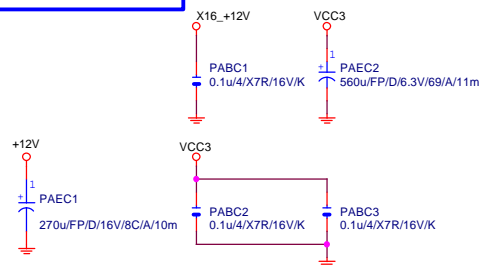
SYSTEM FAN2

SYSTEM FAN3

Title			
FAN CTRL			
Size	Document Number	Rev	
Custom	GA-H110M-D3H	1.0	
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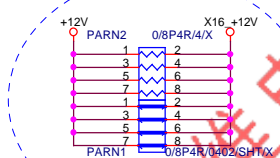
Rev 0.3

PCIEX16 CAP



PCIEX16 PROTECT SHT

+12 protect short-wire test



PCIEX16 AC CAP

PA EXP TXP0	PAC5	0.22u/4/X5R/6.3V/K	PA EXP TXP0 C
PA EXP TXN0	PAC4	0.22u/4/X5R/6.3V/K	PA EXP TXN0 C
PA EXP TXP1	PAC6	0.22u/4/X5R/6.3V/K	PA EXP TXP1 C
PA EXP TXN1	PAC7	0.22u/4/X5R/6.3V/K	PA EXP TXN1 C
PA EXP TXP2	PAC8	0.22u/4/X5R/6.3V/K	PA EXP TXP2 C
PA EXP TXN2	PAC9	0.22u/4/X5R/6.3V/K	PA EXP TXN2 C
PA EXP TXP3	PAC10	0.22u/4/X5R/6.3V/K	PA EXP TXP3 C
PA EXP TXN3	PAC11	0.22u/4/X5R/6.3V/K	PA EXP TXN3 C
PA EXP TXP4	PAC12	0.22u/4/X5R/6.3V/K	PA EXP TXP4 C
PA EXP TXN4	PAC13	0.22u/4/X5R/6.3V/K	PA EXP TXN4 C
PA EXP TXP5	PAC14	0.22u/4/X5R/6.3V/K	PA EXP TXP5 C
PA EXP TXN5	PAC15	0.22u/4/X5R/6.3V/K	PA EXP TXN5 C
PA EXP TXP6	PAC16	0.22u/4/X5R/6.3V/K	PA EXP TXP6 C
PA EXP TXN6	PAC17	0.22u/4/X5R/6.3V/K	PA EXP TXN6 C
PA EXP TXP7	PAC18	0.22u/4/X5R/6.3V/K	PA EXP TXP7 C
PA EXP TXN7	PAC19	0.22u/4/X5R/6.3V/K	PA EXP TXN7 C
PA EXP TXP8	PAC20	0.22u/4/X5R/6.3V/K	PA EXP TXP8 C
PA EXP TXN8	PAC21	0.22u/4/X5R/6.3V/K	PA EXP TXN8 C
PA EXP TXP9	PAC22	0.22u/4/X5R/6.3V/K	PA EXP TXP9 C
PA EXP TXN9	PAC23	0.22u/4/X5R/6.3V/K	PA EXP TXN9 C
PA EXP TXP10	PAC24	0.22u/4/X5R/6.3V/K	PA EXP TXP10 C
PA EXP TXN10	PAC25	0.22u/4/X5R/6.3V/K	PA EXP TXN10 C
PA EXP TXP11	PAC26	0.22u/4/X5R/6.3V/K	PA EXP TXP11 C
PA EXP TXN11	PAC27	0.22u/4/X5R/6.3V/K	PA EXP TXN11 C
PA EXP TXP12	PAC28	0.22u/4/X5R/6.3V/K	PA EXP TXP12 C
PA EXP TXN12	PAC29	0.22u/4/X5R/6.3V/K	PA EXP TXN12 C
PA EXP TXP13	PAC30	0.22u/4/X5R/6.3V/K	PA EXP TXP13 C
PA EXP TXN13	PAC31	0.22u/4/X5R/6.3V/K	PA EXP TXN13 C
PA EXP TXP14	PAC32	0.22u/4/X5R/6.3V/K	PA EXP TXP14 C
PA EXP TXN14	PAC33	0.22u/4/X5R/6.3V/K	PA EXP TXN14 C
PA EXP TXP15	PAC34	0.22u/4/X5R/6.3V/K	PA EXP TXP15 C
PA EXP TXN15	PAC35	0.22u/4/X5R/6.3V/K	PA EXP TXN15 C

PCI-E REV:1.1--> 2.5GHZ

PCE-E X1(單向) BANDWITH=2.5GHz*(8b/10b)=2Gb/s=250MB/s

PCE-E X1(雙向) BANDWITH=2.5GHz*(8b/10b)X2=4Gb/s=500MB/s

PCE-E X16(單向) BANDWITH=2.5GHz*(8b/10b)X16=32Gb/s=4GB/s

PCE-E X16(雙向) BANDWITH=2.5GHz*(8b/10b)X16X2=64Gb/s=8GB/s

PCI-E REV:2.0--> 5GHZ

PCIEX16 SLOT

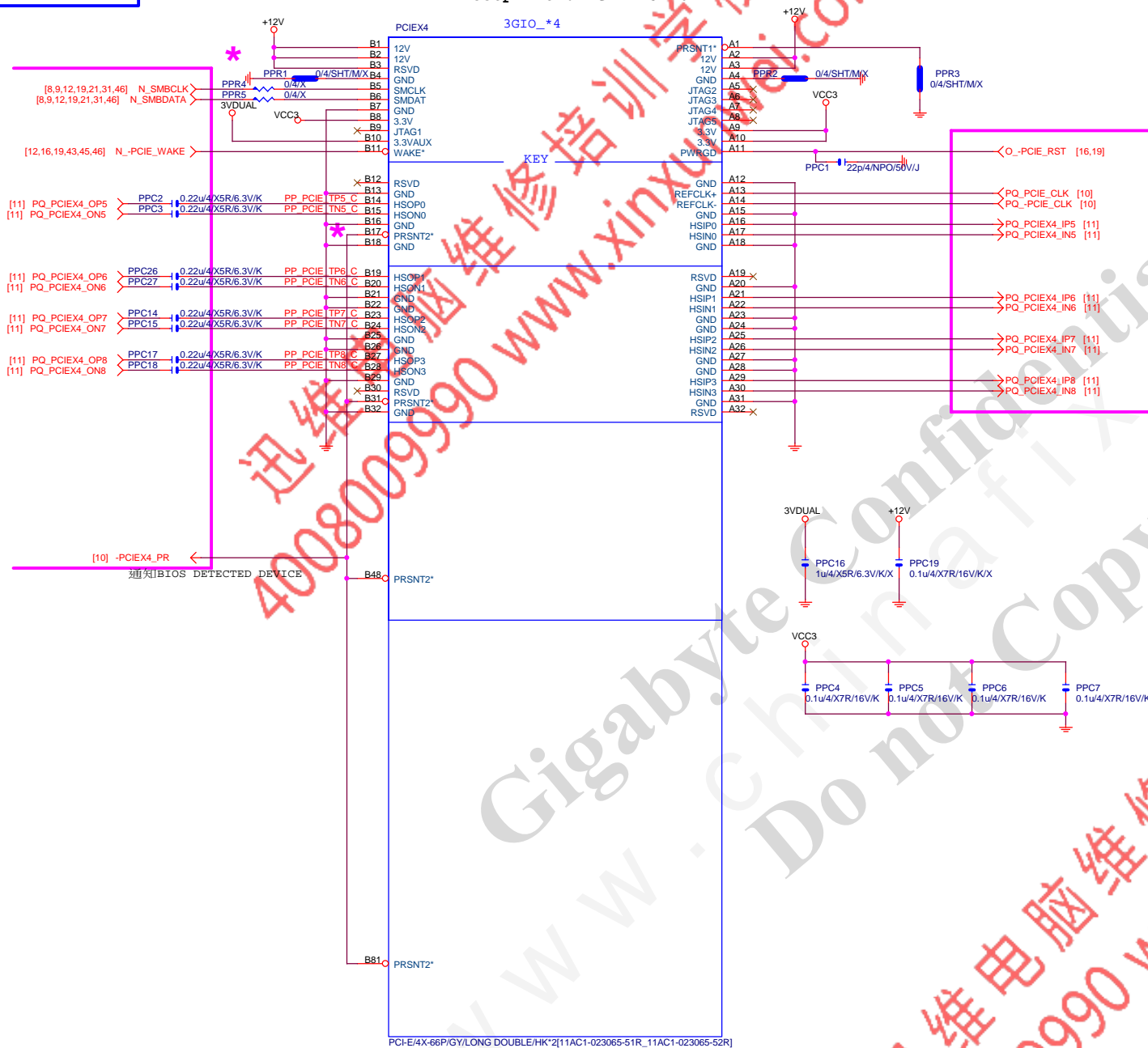


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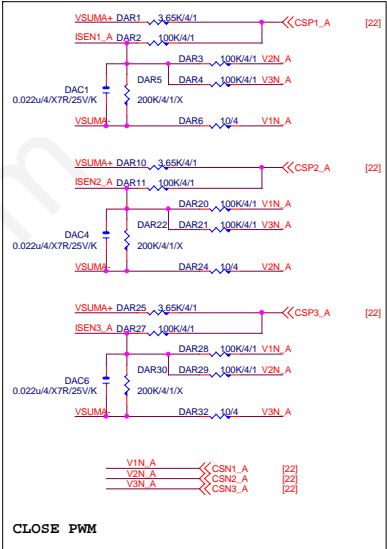
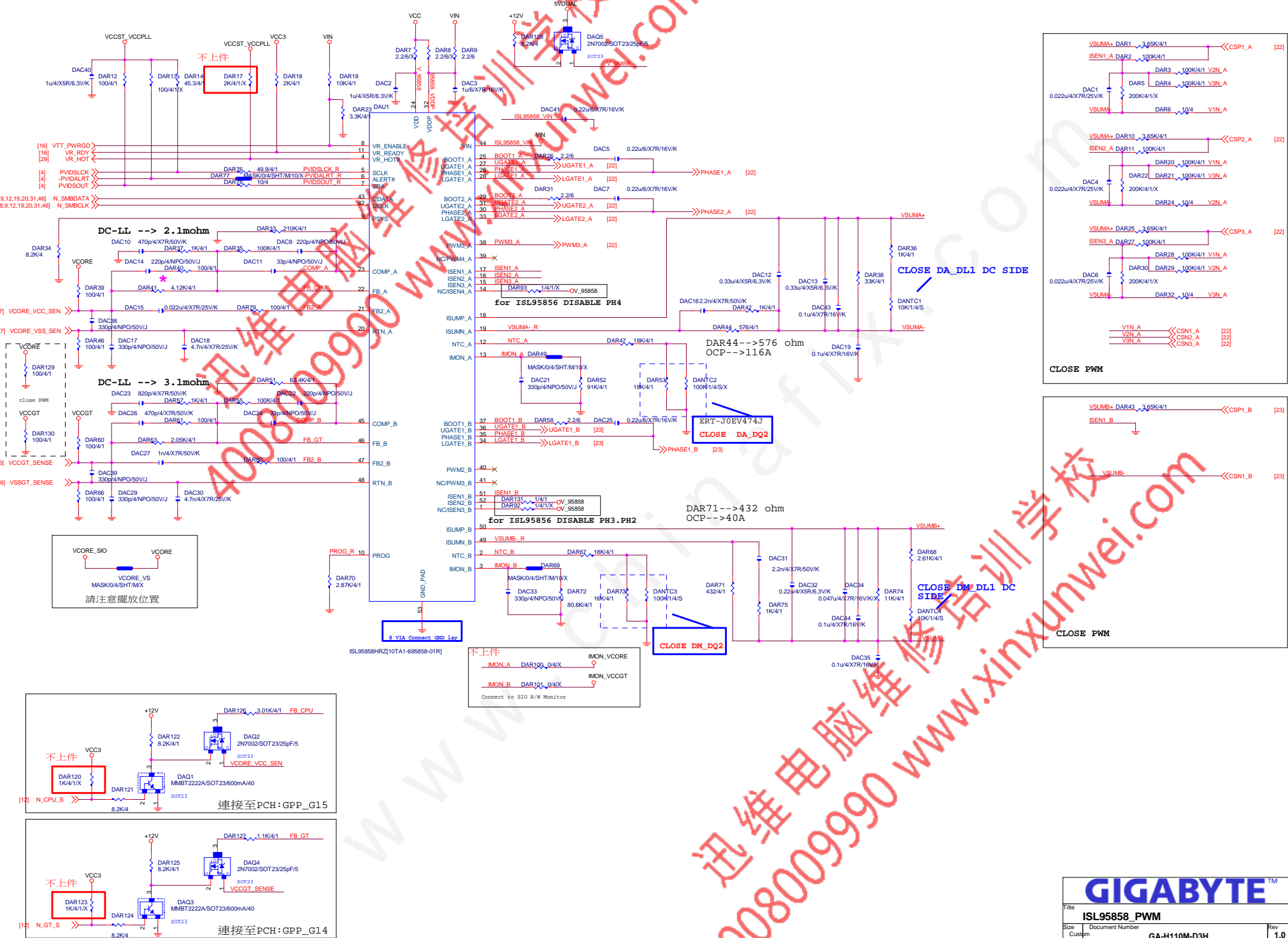
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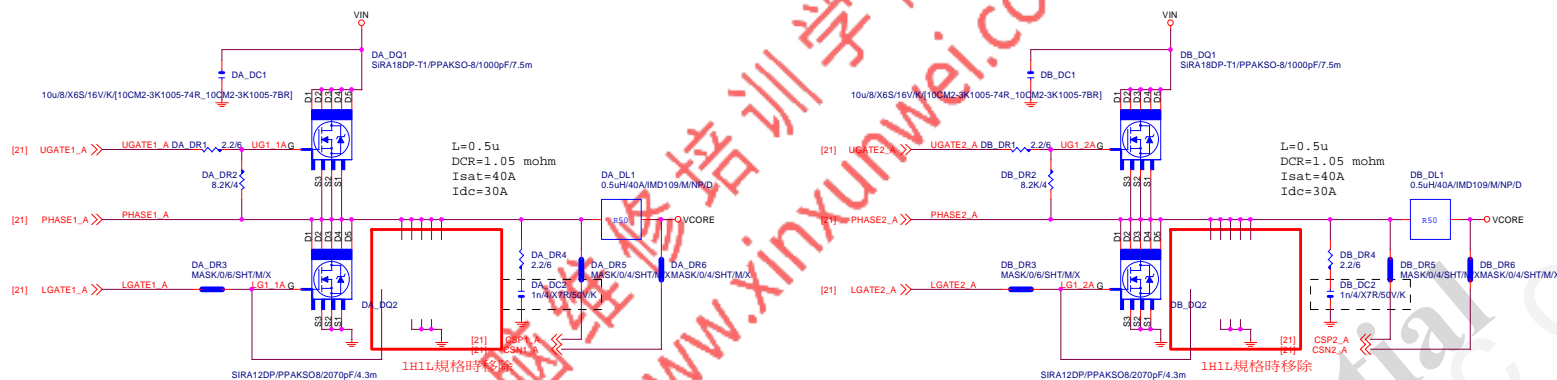
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Custom	GA-H110M-D3H		
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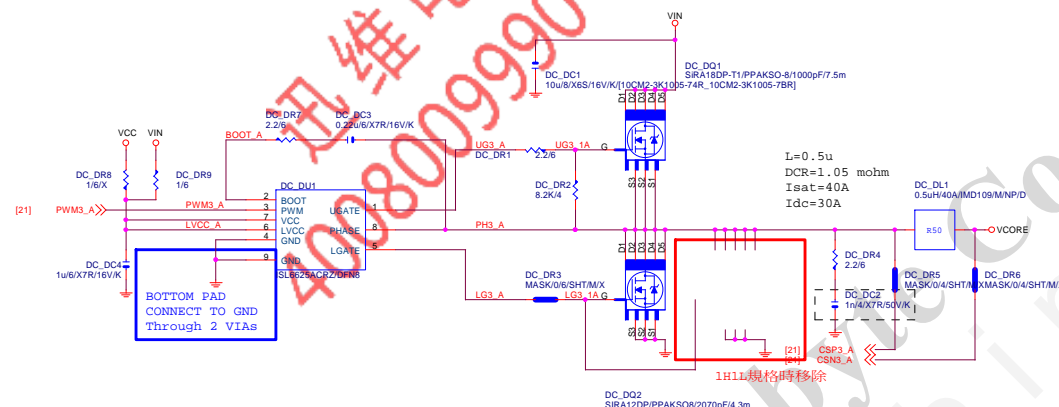
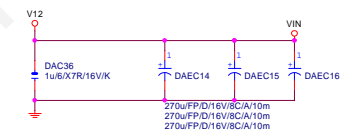
GIGABYTE™		
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Size	Document Number	Rev
Custom	GA-H110M-D3H	1.0
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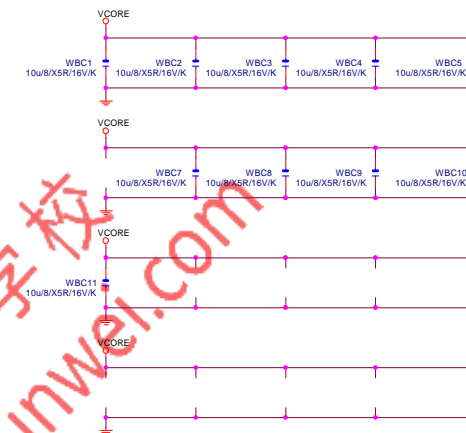
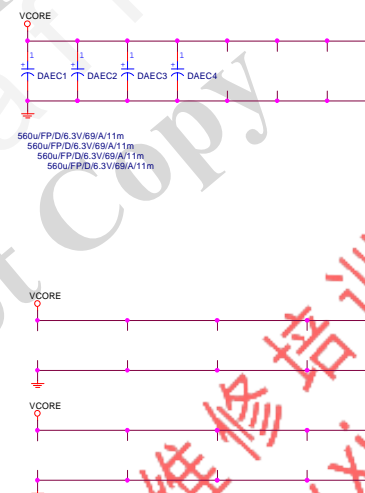
VCORE



VIN CAP 270u*3PCS



VCORE CAP 560u*4PCS
 10u*10PCS

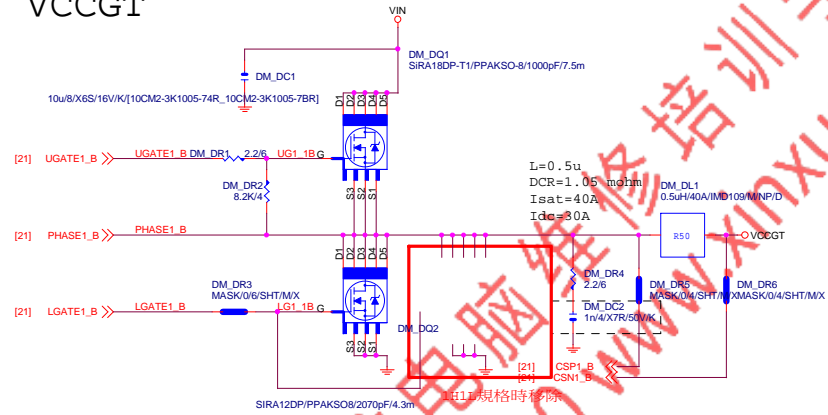


GIGABYTE™

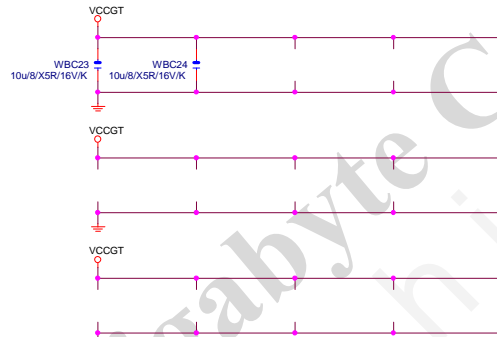
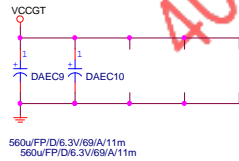
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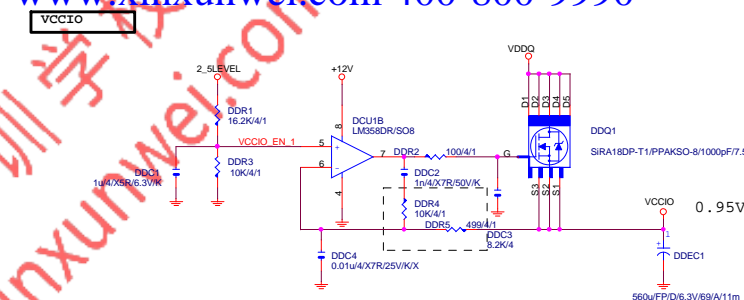
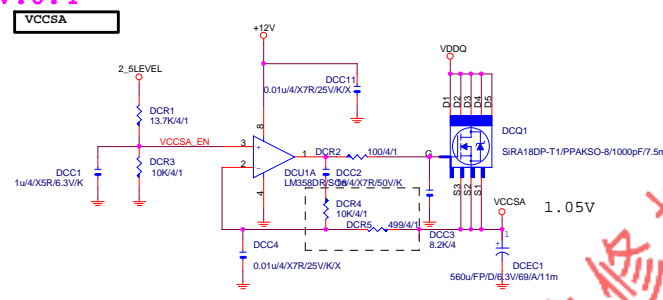


VCCGT

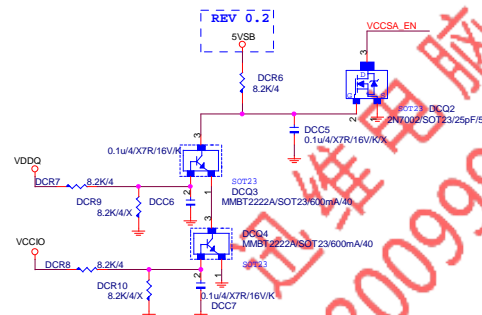


VCCGT CAP 560u*2PCS
10u*2PCS



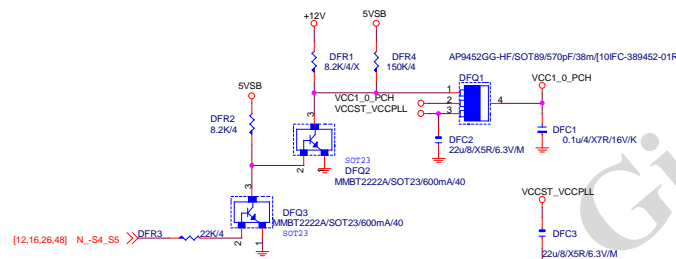


Connect to IT8620

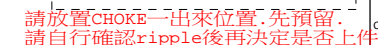


VCCST_VCCPLL

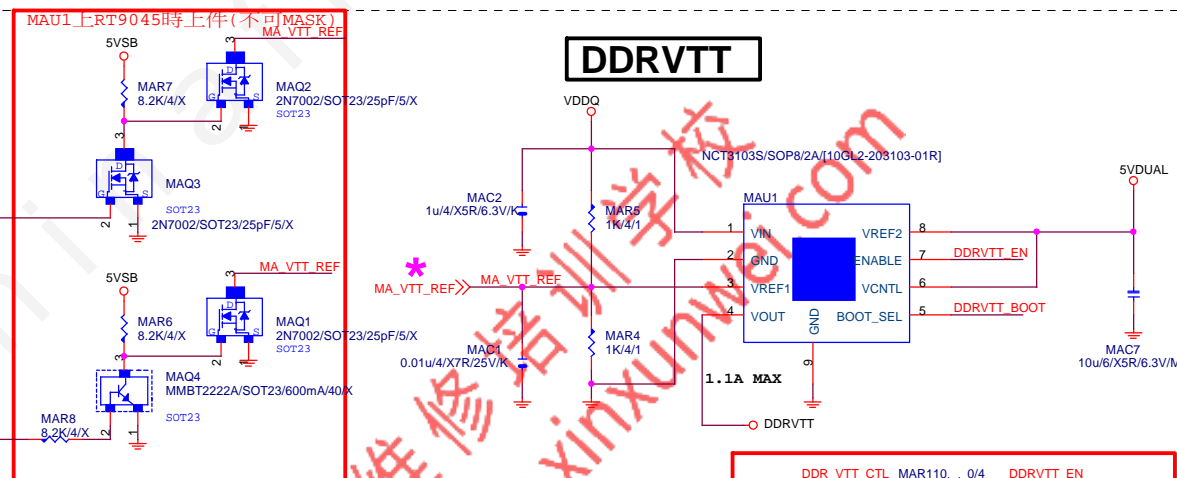
REV: 0.22



DDR VIN CAP
560--*1PCS



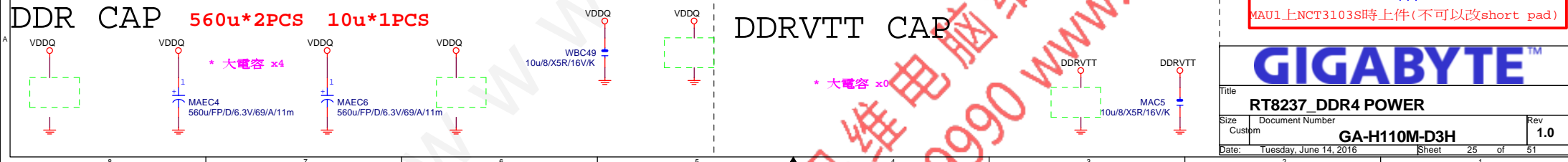
VPP_25V使用8120時上件



DDR_VTT_CTL	MAR110	0/4	DDRVTT_EN
N_SLP_S3	MAR111	0/4	DDRVTT_BOOT

MAU1上NCT3103S時上件(不可以改short pad)

DDRVTT CAP

**GIGABYTE™**

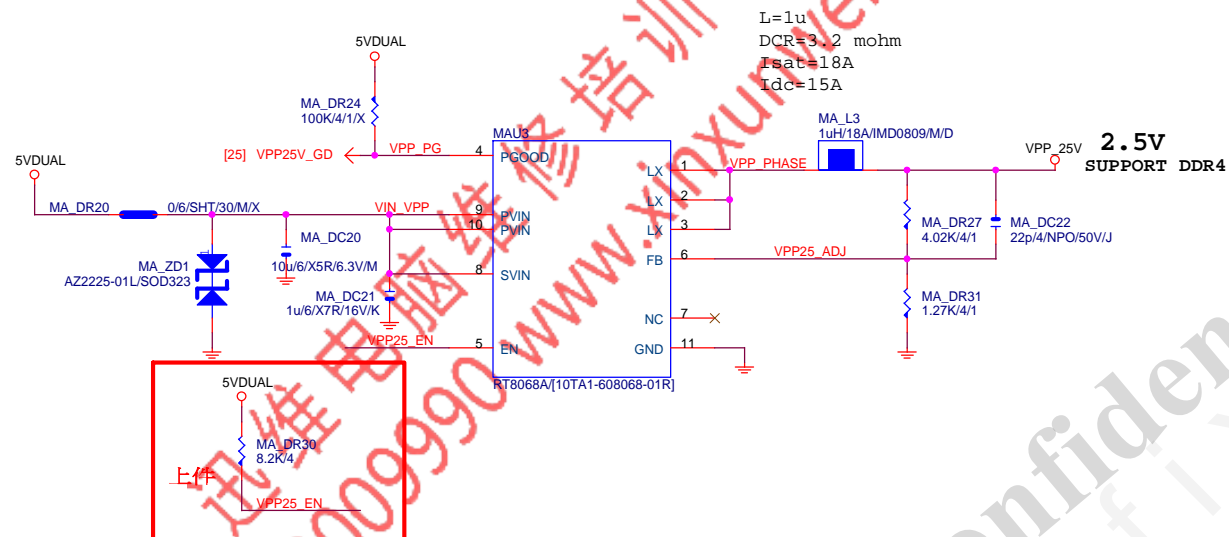
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RT8237_DDR4 POWER			
Size	Document Number	Rev	
Custom	GA-H110M-D3H	1.0	
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REV:0.4

VPP_25V

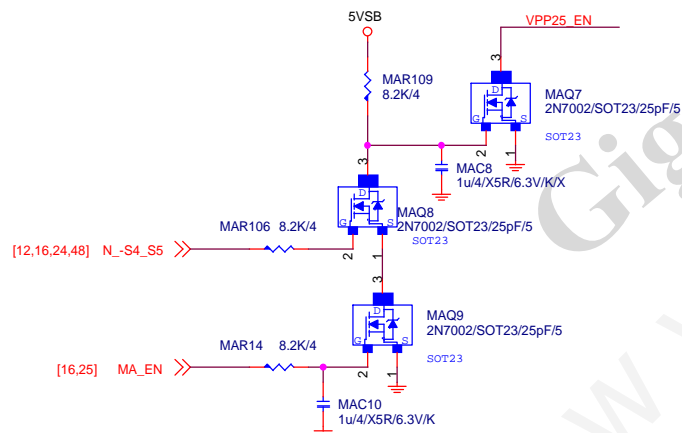
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CHOKER與CAP料號可變



PWR_SEQ

* 刪 MA_DR32



VPP_25V

MAC49 0.1u/4/X7R/16V/K

VPP_25V

MAC50 0.1u/4/X7R/16V/K

VPP_25V

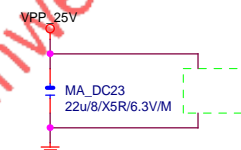
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VPP_25V

MAC52 0.1u/4/X7R/16V/K

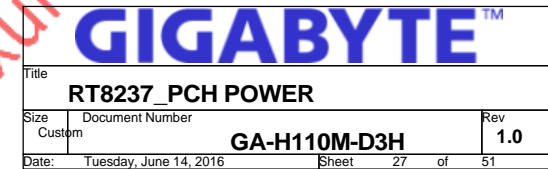
VPP CAP 22u*1PCS

* 大電容 22u



Gigabyte Technology

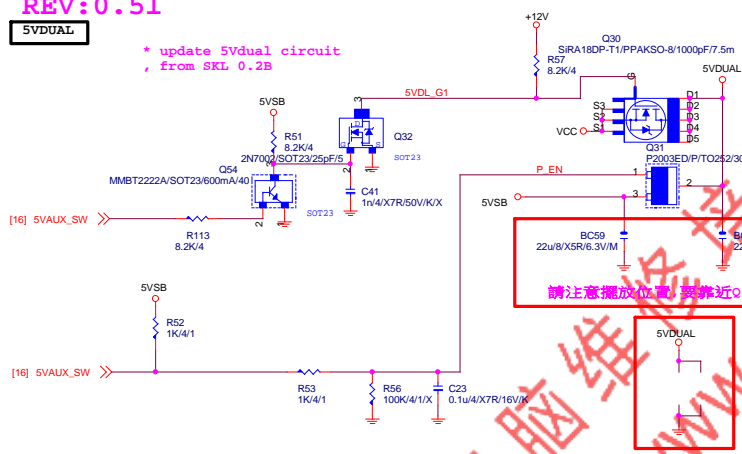
Title			M.2 X2
Size Custom			Document Number
Date:			GA-H110M-D3H
Tuesday, June 14, 2016			Rev 1.0
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REV: 0.51

5VDUAL

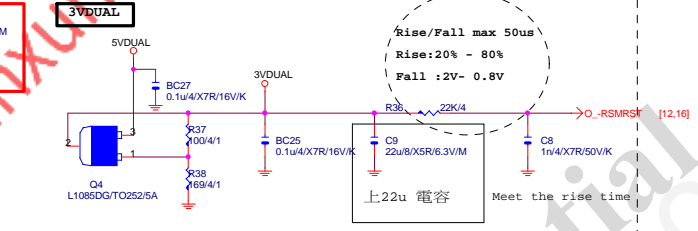
* update 5Vdual circuit
from SKL 0.2B



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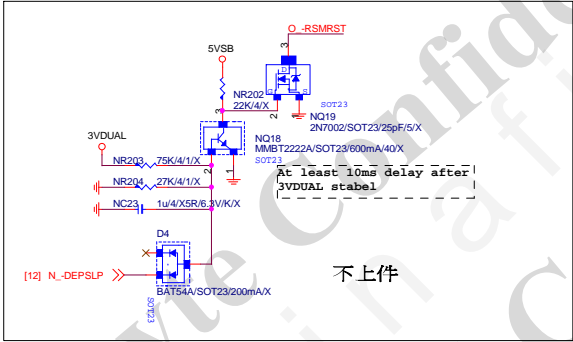
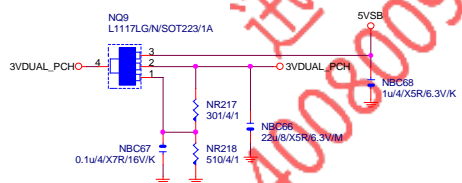
3VDUAL



Rise/Fall max 50us
Rise: 20% - 80%
Fall: 2V - 0.8V

上22u 电容
Meet the rise time

3VDUAL_PCH



不上件

迅维电脑维修培训学校
4008009990 www.xinxunwei.com

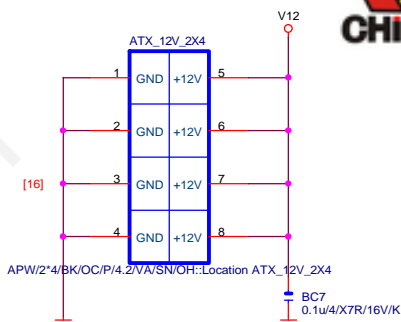
Gigabyte Technology

Title			DISCRETE POWER
Size	Document Number	GA-H110M-D3H	
Custom		Rev	1.0
Date:	Tuesday, June 14, 2016	Sheet	28 of 51

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5VSB

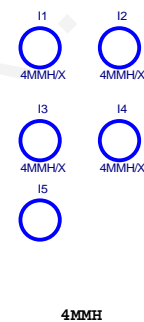
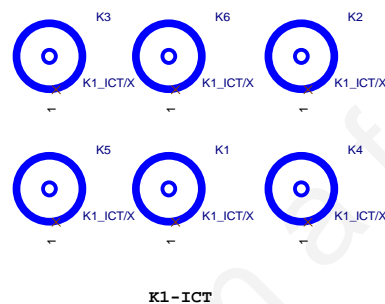
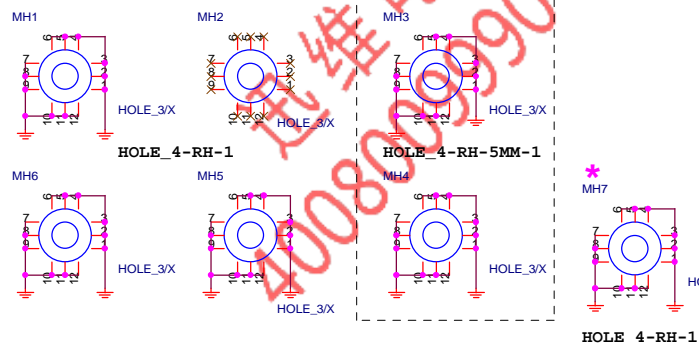
R695



```

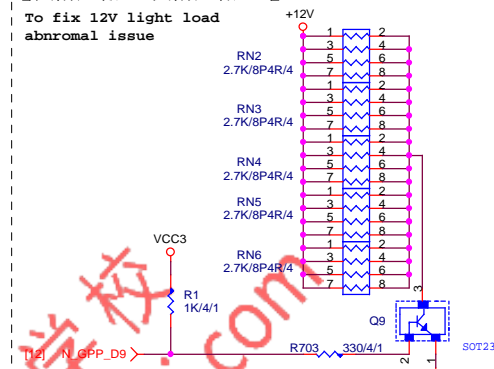
510/6X
To prevent the 5VSB
under loading when
boot-----

```

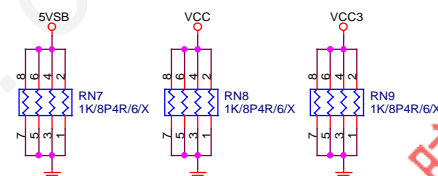


【技術通報R&D技術通報153】

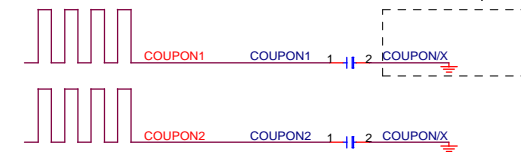
To fix 12V light load
abnromal issue



-PROHOT * 保留？



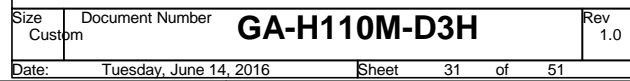
BY LAYOUT位置

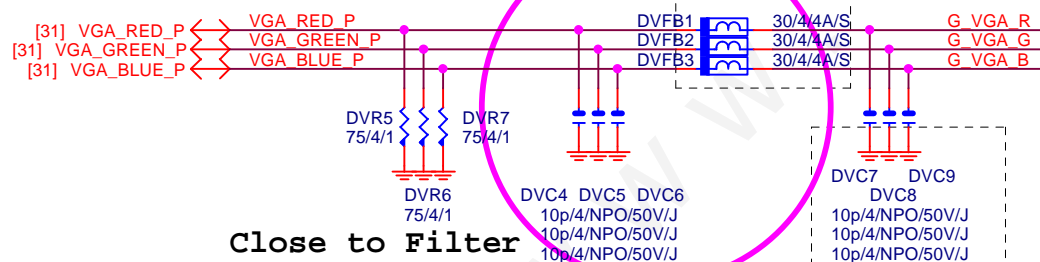
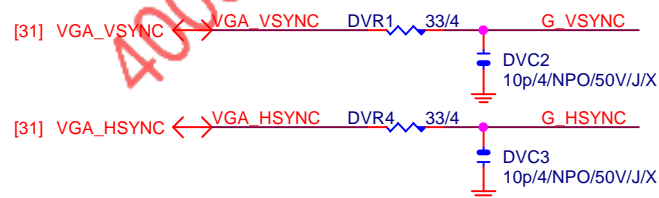
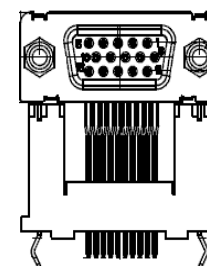


Gigabyte Technology

Title				Rev	
ATX POWER CONNECTOR					
Size	Document Number			Rev	
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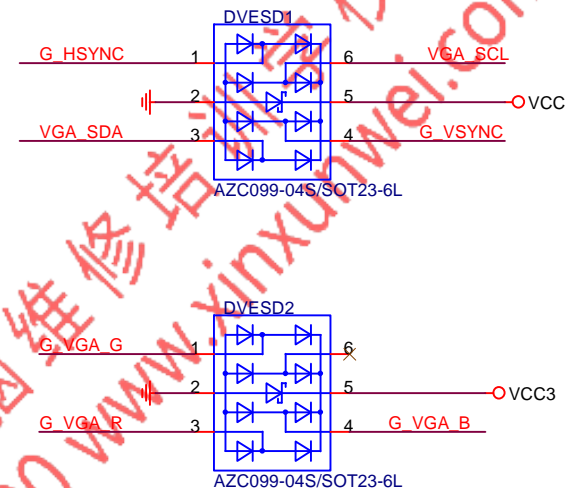




Close to Filter

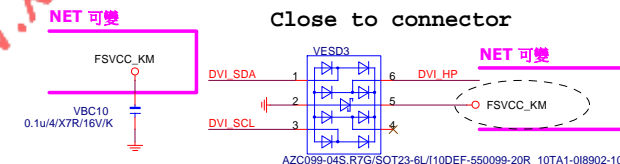
FOR EMI

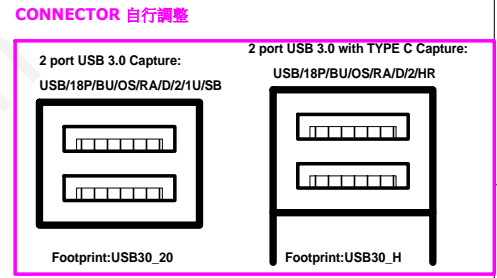
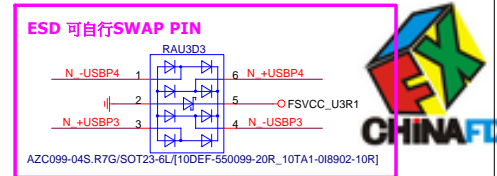
VGA ESD

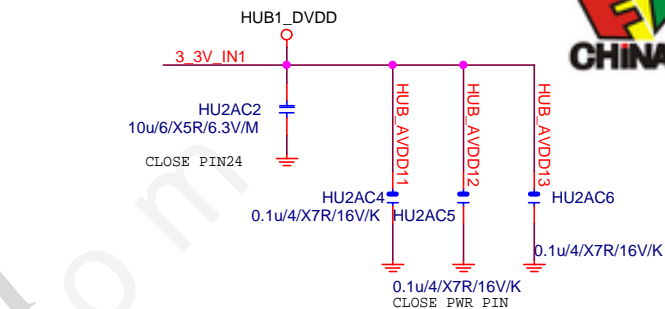


Gigabyte Technology
DP-VGA RTD2168

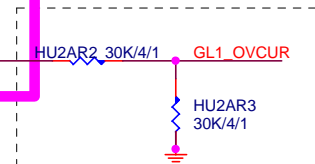
Title		Document Number		Rev
Size		Custom		1.0
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FSVCC_F1



```
P.H.  USE  GL_OVCUR1
DETECT ALL PORT
```

GL1_PSELF

```
HUB self-power mode:
P.H.=EVERY PORT 500mA.
P.D.=EVERY PORT 100mA.
```

Diagram illustrating the crystal circuit for the i.MX27. The circuit includes a crystal (HU2AX1, 12M/20p/30ppm/49US/40/D) connected to two pins, GL1_XTALO and GL1_XTALI. The circuit also includes capacitors HU2AC3 and HU2AC8, and is connected to ground.

Gigabyte Technology

GL850S

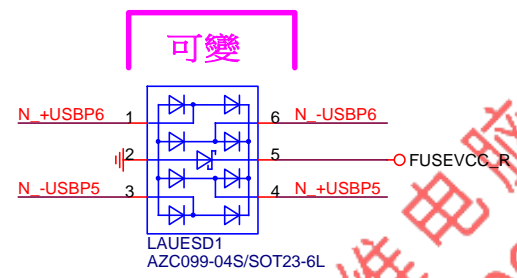
GA-H110M-D3H

Rev	
1.0	

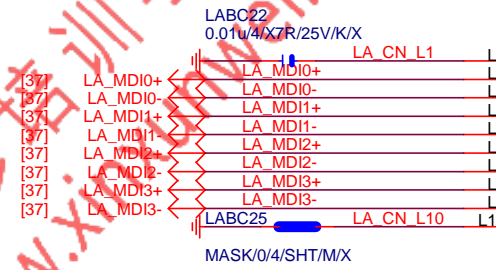
Date: Tuesday, June 14, 2016 Sheet 36 of 51

USB_LAN CONNECTOR R1.06

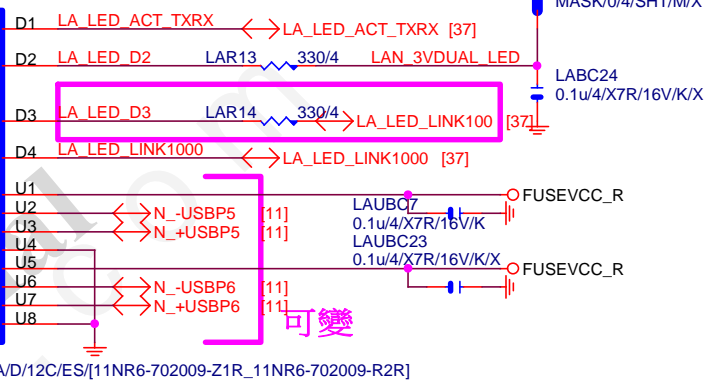
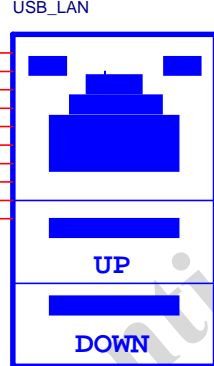
RMA ESD PROTECT note:可變更USB NAME



USB_LAN CONNECTOR

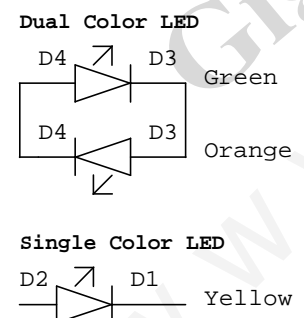
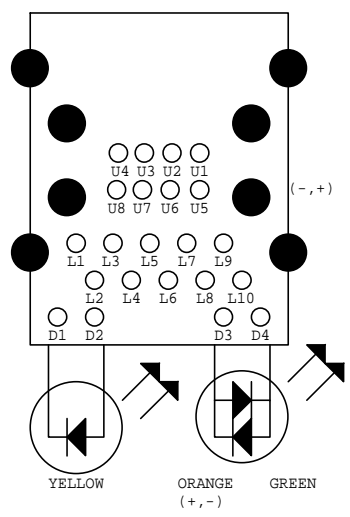


[RTL8111G]

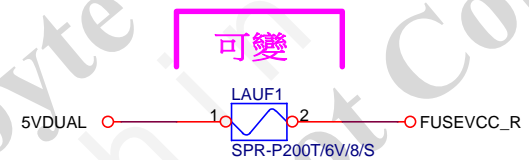


LA_MDI-->100歐姆:[20/4/8/4/20]

USB_LAN LAYOUT示意图

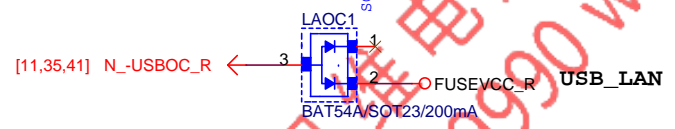


USB POWER note:可變更FUSE



Close to connector
USB_LAN 2-Port 2.0A
FUSE-0805

USBOC

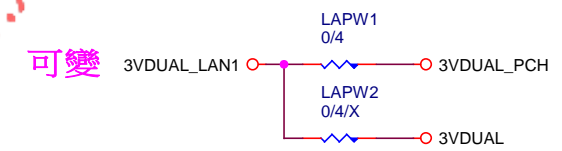


EMI SHORT PAD

PS:視EMI需求



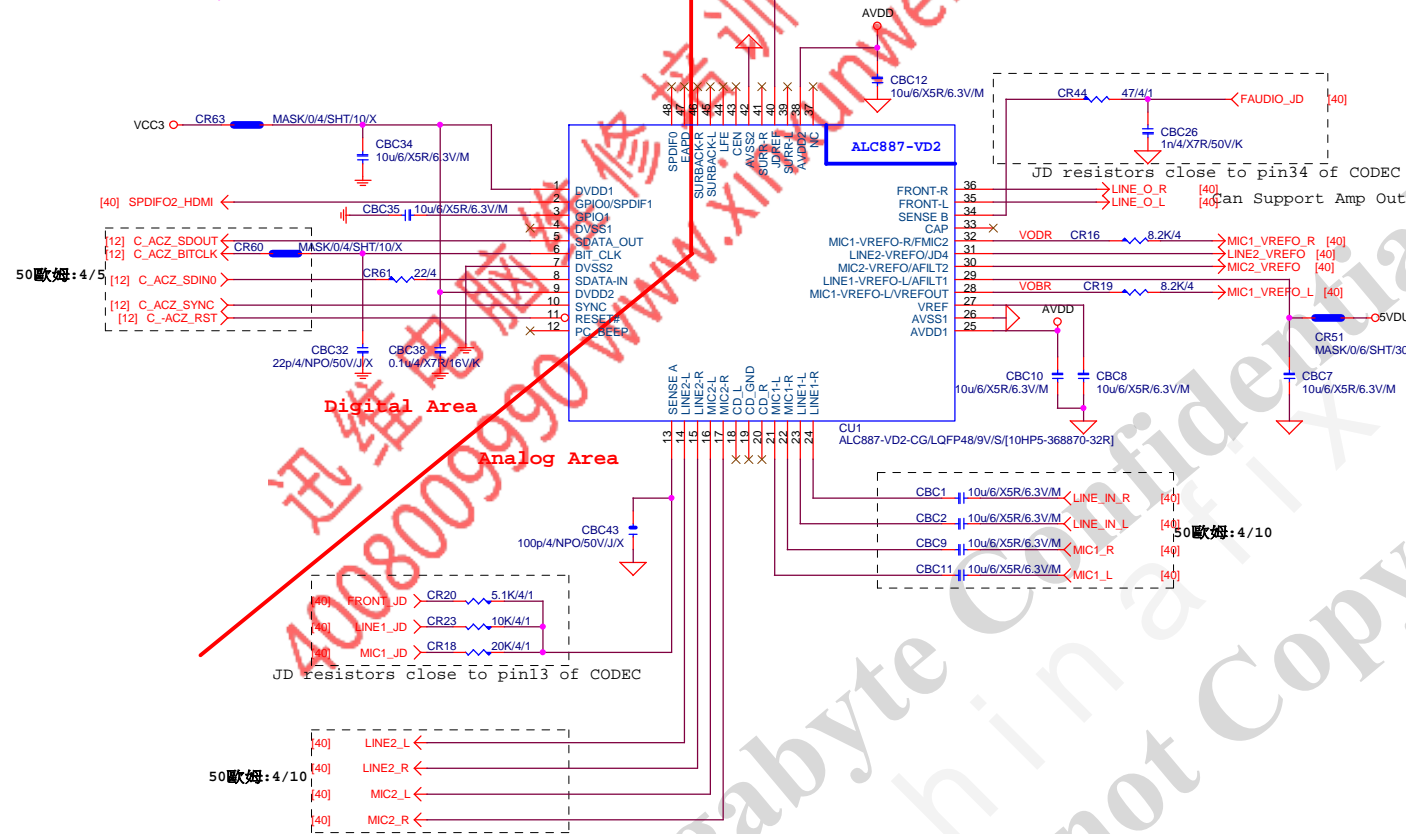
LAN POWER note: lan power連接及電流



Gigabyte Technology
LAN CONNECTOR-8111G
GA-H110M-D3H
Title
Size Custom Document Number
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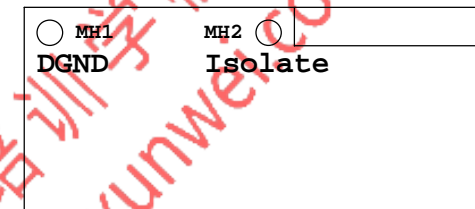


ALC887 三孔 AUDIO JACK

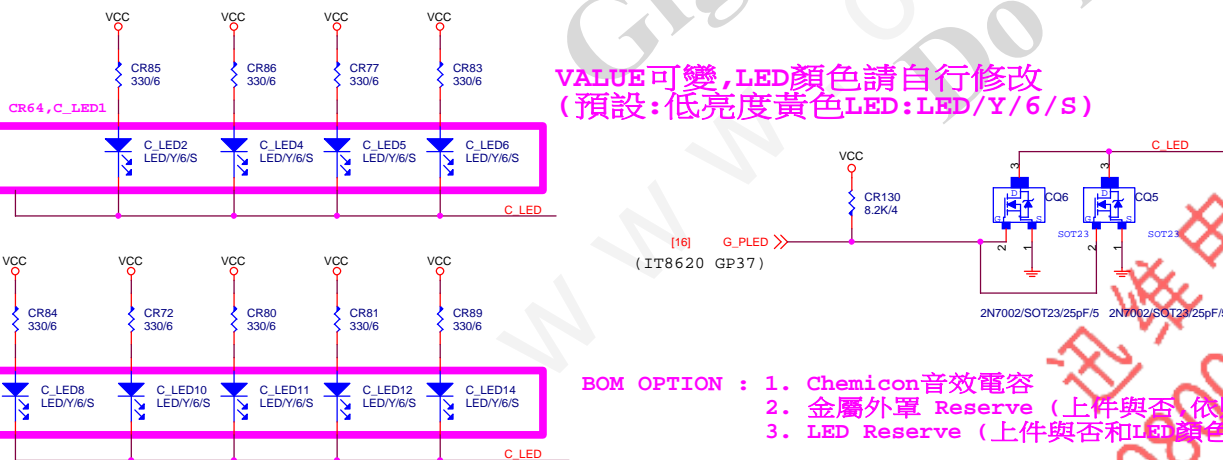


LAYOUT注意:螺絲孔下GND方式

1. MH1空間夠,下DGND
空間不夠,改為Isolate
2. MH2一律改為Isolate



VALUE可變,LED顏色請自行修改
(預設:低亮度黃色LED:LED/Y/6/S)



BOM OPTION : 1. Chemicon音效電容
2. 金屬外罩 Reserve (上件與否, 依照各Model spec)
3. LED Reserve (上件與否和LED顏色, 依照各Model spec)


LAYOUT注意:要加

GND切割線



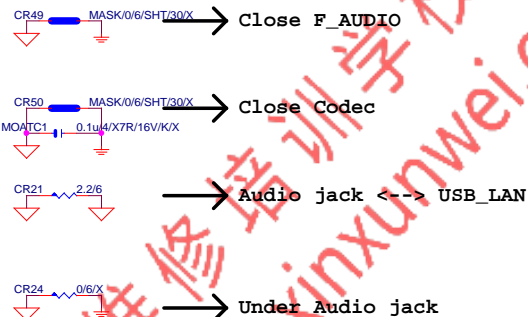
音效區域印刷

*料號後補
*LAYOUT與否,依照各Model spec

				
Title HD AUDIO ALC887				
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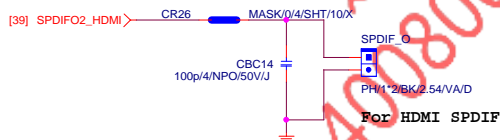
Rev 0.5

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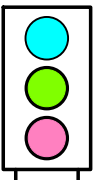
*量產前,0ohm改short pad

SPDIF_OUT



SPDIF_IN

AZALIA JACK

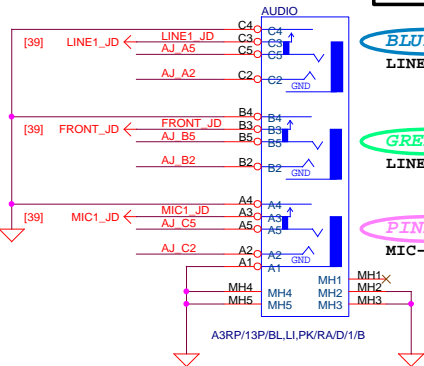


AZALIA JACK

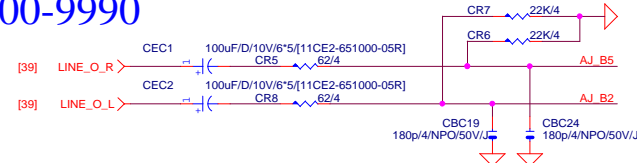
BLUE
LINE-IN

GREEN
LINE-OUT

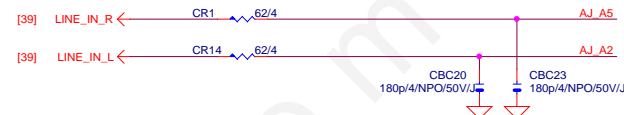
PINK
MIC-IN



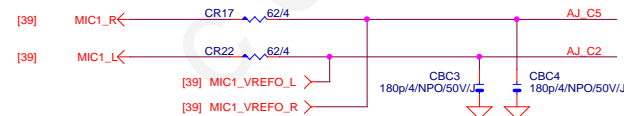
LINE-OUT



LINE-IN



MIC-IN

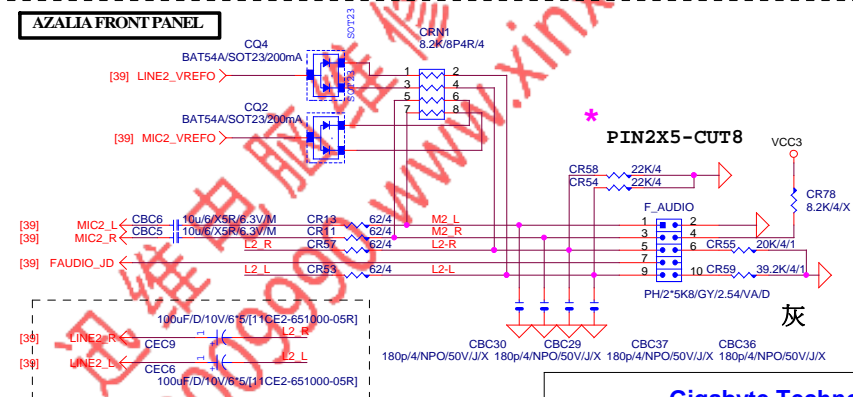


SURROUND

CEN/LFE

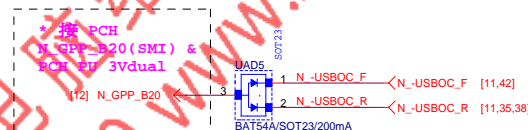
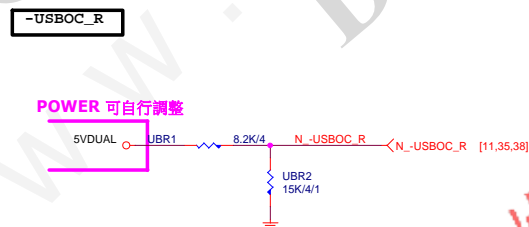
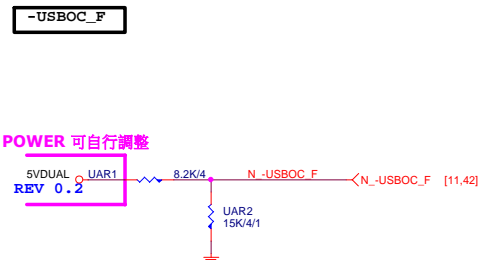
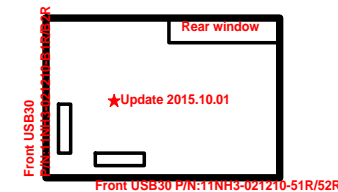
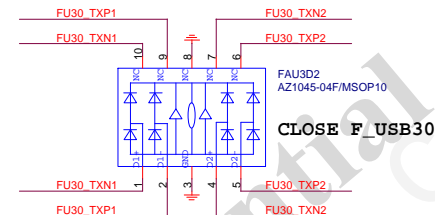
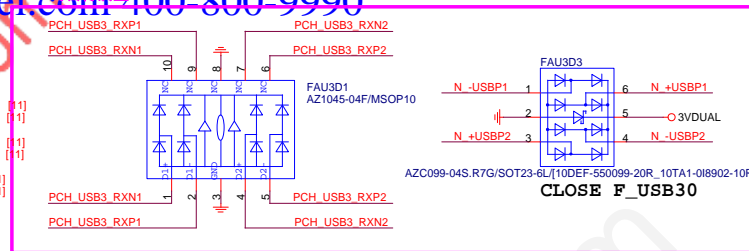
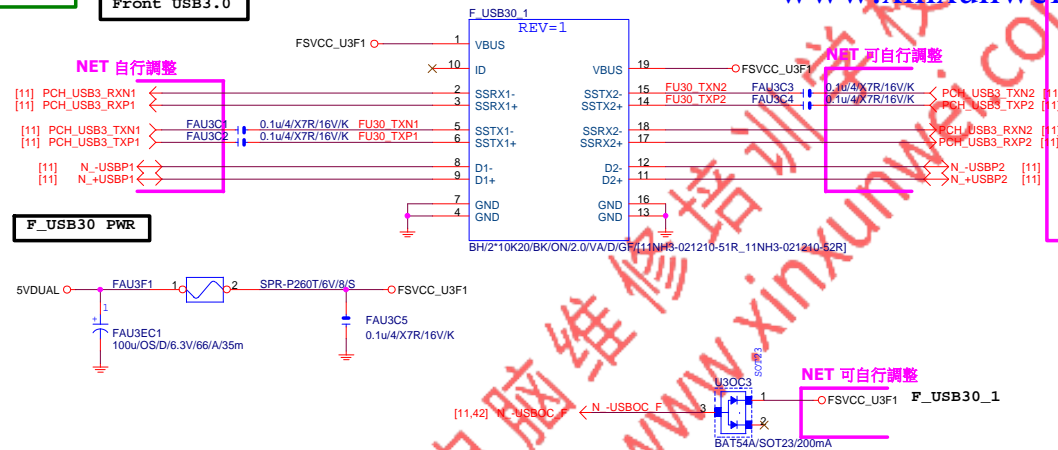
SURRBACK

AZALIAFRONT PANEL



Gigabyte Technology

Title		
AUDIO JACK		
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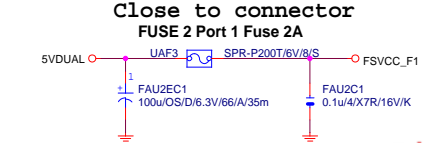
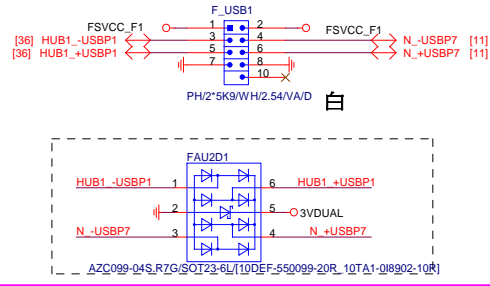




FRONT USB1

NET 可變

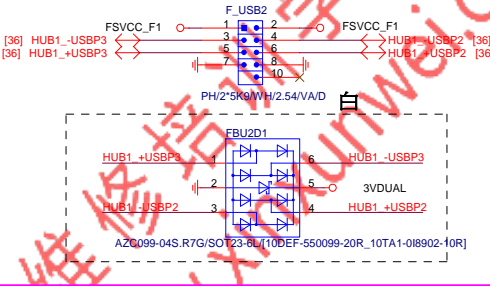
FUSB2X5-S



FRONT USB2

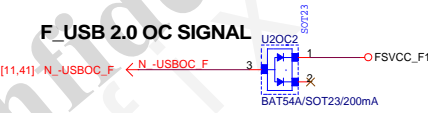
NET 可變

FUSB2X5-S



Del FSVCC1_F2

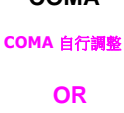
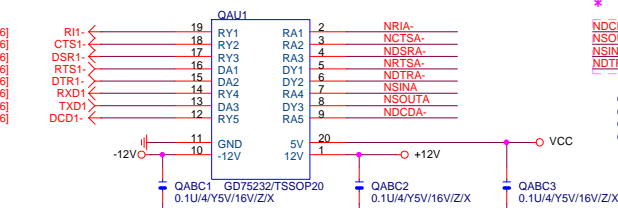
F_USB 2.0 OC SIGNAL



Gigabyte Technology

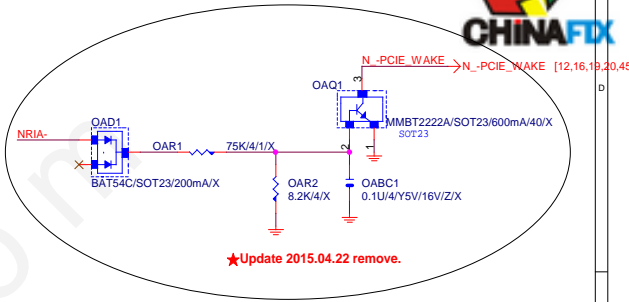
USB2.0

Size	Document Number	GA-H110M-D3H	Rev
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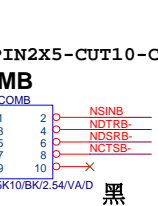
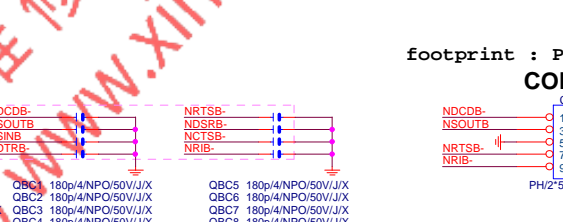
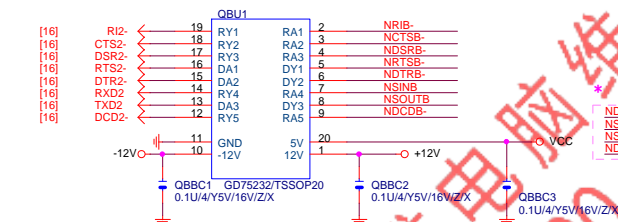


COM/GE/SC-6mm/RA/1/D[11NR6-111009-T1R]

Footprint : COM-HIGH_RISE



★Update 2015.04.22 remove.

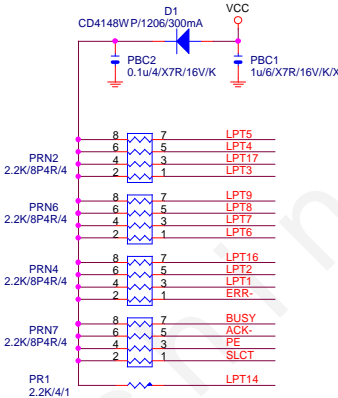
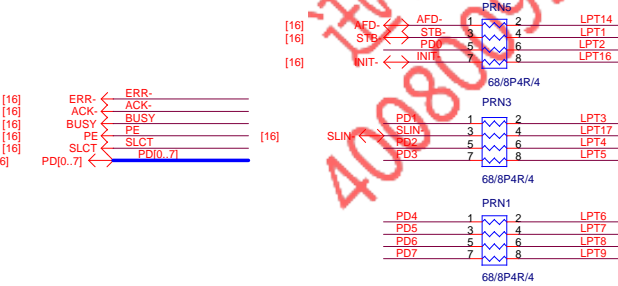


footprint : PIN2X5-CUT10-COM

COMB

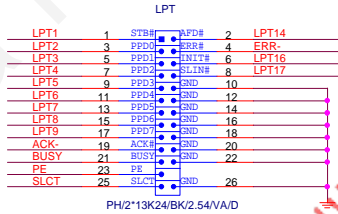
黑

LPT PORT



R&D技術通報151 有使用PRINT PORT的
MODEL，需使用新料號：10HP2-118728-72R。(CHIP IT8728F/EX (GB) ITE/SMD
QFP128 PRINTPORT SORTING)料件。串電阻33 ohm改為68 ohm。

★Update 2015.03.24



footprint :
PIN2X13_LPT-CUT24

黑

TPM CONNECTOR

*Delete TPM PIN Header

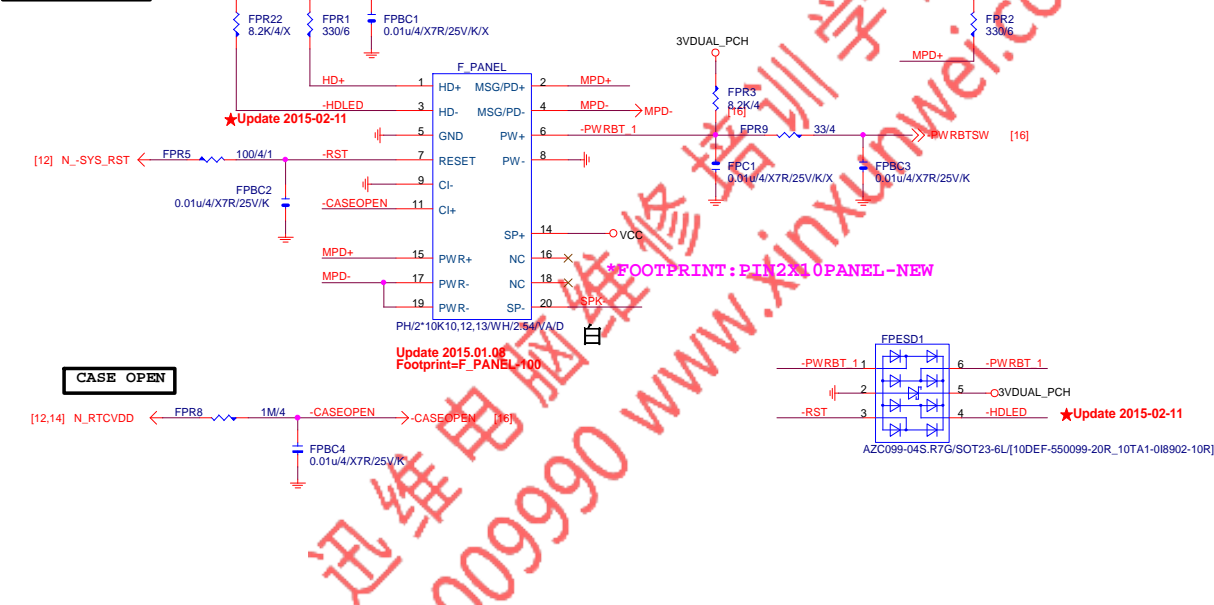
Thunderbolt

BIOS_PH

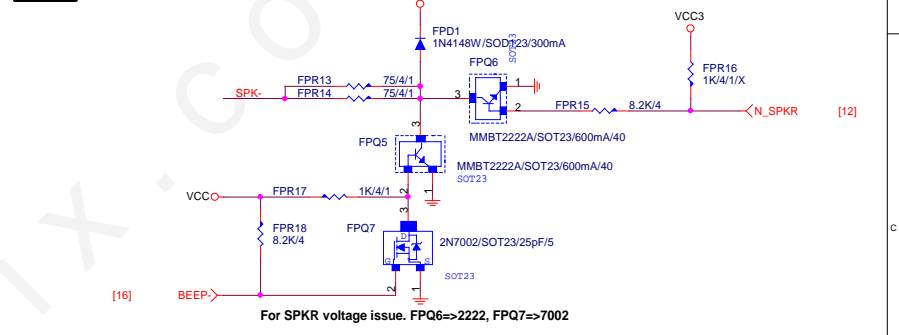
Gigabyte Technology			
Title			
COM,LPT			
Size	Document Number	GA-H110M-D3H	
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FRONT PANEL



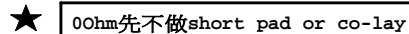
SPKR



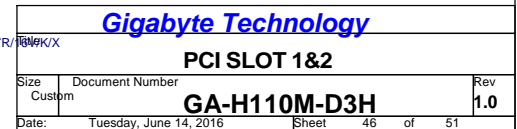
FRONT PANEL SHORT



Gigabyte Technology			
Title			
FRONT PANEL			
Size	Document Number	GA-H110M-D3H	Rev
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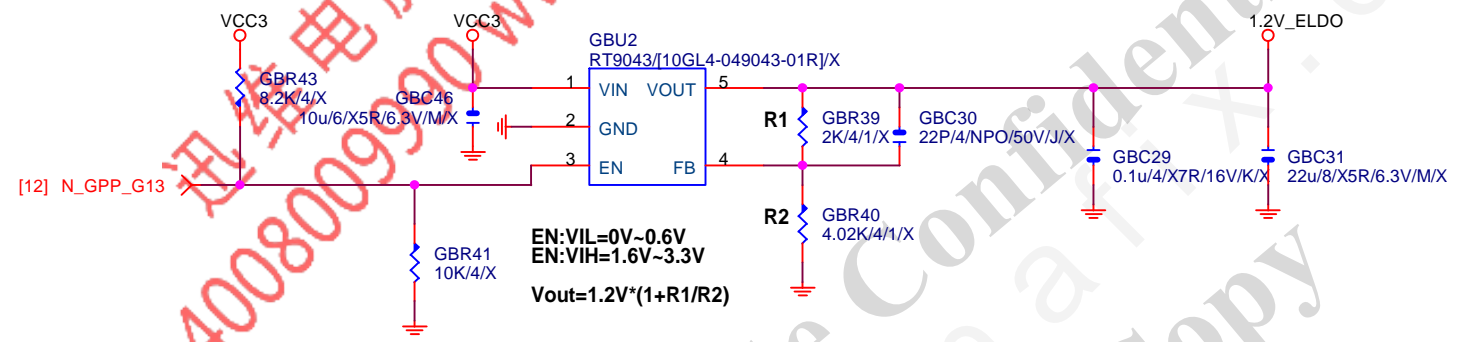
	component change note
IT8892FX	GR70, GR74, GR76, GR78, GR66 : ON GR69, GR73, GR75, GR77, GR67 : NC GR44 resistor is 1.8k ohm GL14, GL10, GL16, GL17 : ON GL19, GL21, GL23, GL25 : NC
IT8892JX	GR70, GR73, GR75, GR78, GR66 : ON GR69, GR74, GR76, GR77, GR67 : NC GR44 Resistor is 1.8k ohm GL14, GL10, GL16, GL17 : ON GL19, GL21, GL23, GL25 : NC
External LDO Power (IT8892JX)	GR69, GR73, GR75, GR77, GR67 : ON GR70, GR78, GR66 : NC GR44 resistor is 1.8k ohm GL19, GL21, GL23, GL25 : ON GL14, GL10, GL16, GL17 : ON





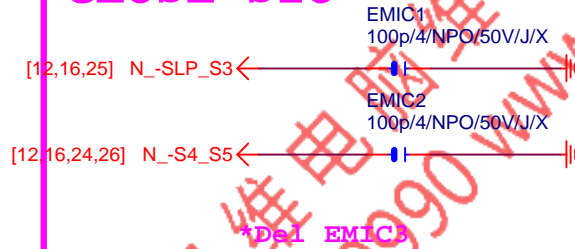
Rev 0.1

* 全部不上件



Gigabyte Technology		
Title IT8892 POWER		
Size Custom	Document Number GA-H110M-D3H	Rev 1.0
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CLOSE SIO



CLOSE PCH



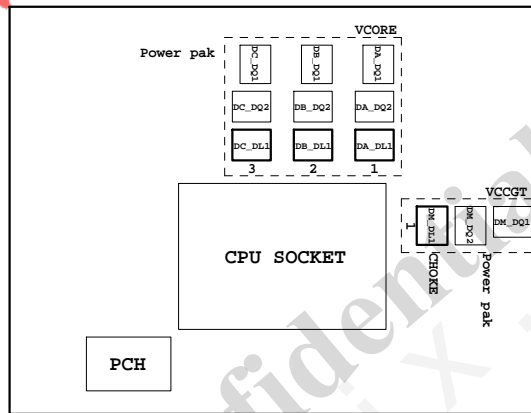
CLOSE AUDIO



GIGABYTE™

Title		
EM/ESD		
Size A	Document Number GA-H110M-D3H	Rev 1.0
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Super I/O	Pin Name	GPIO Table	Usage	Note
	PCIRST#3/GPIO1/UTIMM_SR_EN		N/A	
	PCIRST#3/GP11		0 - PCIR_RST	
	PCIRST#10/GP12		0 - PFMSTP2	
	SVC/FPC1_RST/GP14		N - THRMTRIP	
SLP	RUSB/PCIRATIN/CIRTX2/GP15		-PCIRATIN	
	PS1_L/FAN_CTL5/CIRRX2/GP16		0 - PS1	
	RI2H/GP17		RI2-	
	TBR_PWM_CTSD2H/GP20		CTSD2-	
IO	EM1DCD28H/GP21		DCD2-	
	SP1_S1/GP22		BEEP-	
	DPWROK/CPU_PG/GP23		N_PCH_DPWROK	
	FAN_TACS/RSR2H/GP24		RTS2-	
	FAN_TAC4/DSR2H/GP25		DSK2-	
	INV_OUT1/OUT2/GP26		TXD2	
	INV_IN1/SIN2/GP27		RXD2	
	ATXPG/GP30		PWOK	
	CTS1/GP31		CTS1-	
	OCWD13/RJ11H/GP32		RI1-	
	OCWD12/BCD1H/GP33		DCD1-	
	VTT_FWRGD/GP34		VTT_FWRGD	
	VCC1B_EN/GP35		VCC10B_EN	
	FAN_CTL3/GP36		0 - FAN	
	FAN_TAC3/GP37		Q_FLED	
	3VSBSSW/GP40		0 - FAN	
	OCWD1/SIN1/GP41		RXD1	
	GP42/SCK/FAN_CTL4		N - THRMTRIP	
	PANSWH8/GP43		-PWRSW8	
	PWRONH/GP44		Q_PWRRTSW	
	OCWD17/DSK1H/GP45		DSK1-	
	CE1_N/GP47/GP6		CEB_N	
	GP50/JFL		0 - FAN	
	FAN_CTL2/GP51		FANPW2	
	FAN_TAC2/GP52		FANIO2	
	SUSCON/GP53		N - S4_S5	
	PMBH/GP54		N - LPCPME	
	BSMRSTH/CIRRX1/GP55		0 - BSMRST	
	MCLK/FAN_TAC5/GP56		MCLK	
	MDAT/FAN_CTL6/GP57		MDAT	
	KCLK/GP60		KCLK	
	KDAT/GP61		KDAT	
	KRSTW/GP62		N_KRSTW	
	HOLD_BH/GP63		-SPI_HOLD_B	
	HOLD_BH/GP64		-SPI_HOLD_N	
	VLDT_EN/PCH_D0/GP65		MD_ID2	
	VCC1_05_EN/GP66		VCC1_0_BN	
	GP67		GP67	
	USB_F51/PD0/GP70		PD0	
	USB_F52/PD1/GP71		PD1	
	USB_F53/PD2/GP72		PD2	
	USB_F53/PD3/GP73		PD3	
	USB_F55/PD4/GP74		PD4	
	USB_F56/PD5/GP75		PD5	
	USB_F57/PD7/GP76		PD6	
	USB_F58/PD8/GP77			
	LS-IN1/SLCT/GP80		SLCT	
	LS_OUT1/PE/GP81		PE	
	LS_IN2/BUSY/GP82		BUSY	
	LS_OUT2/ACKH/GP83		ACK-	
	IPHONE_CHARGE#/SLINH/GP84		SLINH-	
	OC_IN/INITH/GP85		INIT-	
	OC_OUT/APDH/GP86		APDH-	
	USB_OC2/STBH/GP87		OCN-	
	DDR_EN/GP90		MA_EN	
	PWRLED/GP91		WPD-	
	HOLD_OUT/GP92		0 - FAN	
	HLDED_IN/GP93		GP93	
	PROCHOTH/GP94		-PROCHOT_CON	
	CPUPWRMGD/GP95		GP95	
	PCH_VRMPWRMGD/GP96		N_PCH_VRMPWRGD	
	VR_RDY/GP97		VR_RDY	



H110M-D3H :
PCH : 12SP2-030005-51R/52R/53R
MOSFET :

H110M-D3H :
PCH : 12SP2-030005-51R/52R/53R
MOSFET :

	Controller
D1	IT8628
D2	IT8628
	NCT3941

 Gigabyte Technology			
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固態電容料號.請自行修改

日系黑色固態	Capture Value
11C02-C85600-01R	560u/FP/D/6.3V/68/C/8m
11C05-C82700-01R	270u/FP/D/16V/88/C/12m
11C05-C61000-01R	100u/OS/D/16V/66/C/30m
11C02-C51000-01R	100u/FP/D/6.3V/65/C/13m

日系一般固態	Capture Value
11C02-685600-01R	560u/FP/D/6.3V/68/8m
11C05-882700-01R	270u/FP/D/16V/88/12m
11C05-661000-03R	100u/OS/D/16V/66/30m
11C02-651000-02R	100u/OS/D/6.3V/66/30m

台系固態	Capture Value
11C02-661000-09R	100u/OS/D/6.3V/66/A/35m
11C05-691000-09R	100u/OS/D/16V/69/A/35m
11C05-8C2700-09R	270u/FP/D/16V/8C/A/10m
11C02-695600-09R	560u/FP/D/6.3V/69/A/11m

IRON CHOKE

	料號	Capture Value	SIZE	Footprint
DIP	11LC5-M4500C-01R	0.5uH/40A/IMD109/M/D	10*10	CHOKE05U-40A-1PQ-3
DIP	11LC5-M2500C-01R	0.5uH/20A/IMD0809/M/D	8*8	CHOKE1U-R50M-IF

Ferrite

	料號	Capture Value	SIZE	Footprint
DIP	11LC5-F3500C-11R	0.5uH/32A/INCG109/FSI/D	10*10	CHOKE05U-40A-1PQ-3
DIP	11LC5-F2500C-11R	0.5uH/25A/INC0809/F/D	8*8	CHOKE1U-R50M-IF
SMD	未建(SIUC1007-R30M-JJ1W)		10*7	CHOKE11X8MM-SMD

BEAD

	料號	Capture Value	SIZE	Footprint
DIP	10LFB-15470A-01R	47/4030/15A/S	4*3	BEADC8B-BPH_SMD

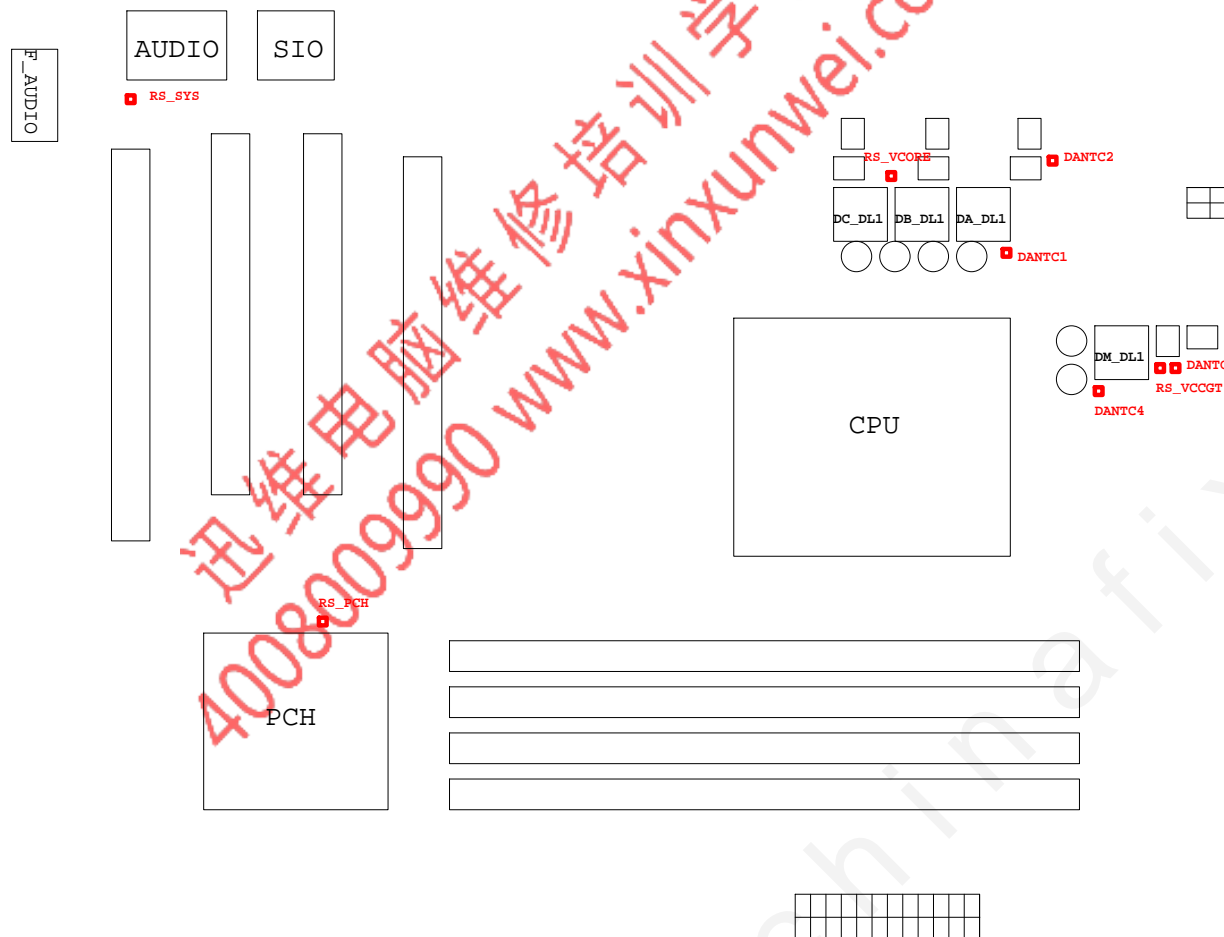
PWM料號

		料號	Capture Value	Footprint
PWM	ISL95856	10TA1-695856-01R		IC52QFN-6x6-G
PWM	ISL95858	10TA1-695858-01R		IC52QFN-6x6-G
PWM	IR35201	10TA1-635201-00R		IC56QFN-9VRS4339
PWM	IR3570	10TA1-603570-00R		IC40MLFP-ISL95835

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熱敏電阻	擺放靠近位置	走線方式
DANTC1	DA_DL1	Differential
DANTC2	DA_DQ1	Differential
DANTC3	DM_DQ1	Differential
DANTC4	DM_DL1	Differential
RS_VCORE	DB_DQ2	N/A
RS_VCCGT	DM_DQ2	N/A
RS_PCH	PCH	N/A
RS_SYS	CU1	N/A